

# Service Manual



ORDER NO. RRV2297

# DV-C503

#### THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Type	Model	Dower Poquirement	Pagion No.	Remarks	
Туре	DV-C503	Power Requirement	Region No.	nemarks	
KUXQ	0	AC120V	1		
KCXQ	0	AC120V	1		
RDXQ1/RA	0	AC110-127 / 220-240V	1		
RDXQ/RD	0	AC110-127 / 220-240V	4		

#### **CONTENTS**

1. SAFETY INFORMATION	2
2. EXPLODED VIEWS AND PARTS LIST	·· 3
3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM $\cdots$	10
4. PCB CONNECTION DIAGRAM ······	31
5. PCB PARTS LIST ·····	44
6. ADJUSTMENT ······	48

7. GENERAL INFORMATION ······	· 49
7.1 DIAGNOSIS	· 49
7.1.1 SELF-DIAGNOSTIC FUNCTION OF	
PICKUP DEFECTIVE ·····	. 49
7.1.2 TEST POINTS LOCATION	· 50
7.1.3 TEST MODE SCREEN DISPLAY ·······	· 51
7.1.4 ERROR CODE	· 55
7.1.5 DISASSEMBLY ······	· 59
7.1.6 TROUBLE SHOOTING	
FOR MECHANISM SECTION	· 71
7.1.7 TROUBLE SHOOTING	
FOR ELECTRICAL SECTION	· 74
7.2 PARTS	· 75
7.2.1 IC ·····	· 75
7.2.2 FL DISPLAY	· 88
B. PANEL FACILITIES AND SPECIFICATIONS	. 89

#### 1. SAFETY INFORMATION

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

#### WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

#### NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols — (fast operating fuse) and/or — (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

#### **REMARQUE**

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible — (fusible de type rapide) et/ou — (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

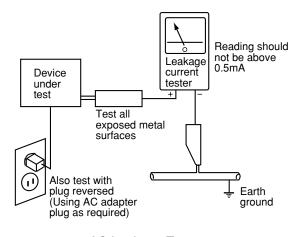
#### (FOR USA MODEL ONLY) \_

#### 1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

#### **LEAKAGE CURRENT CHECK**

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.



AC Leakage Test

ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

#### 2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a  $\Delta$  on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

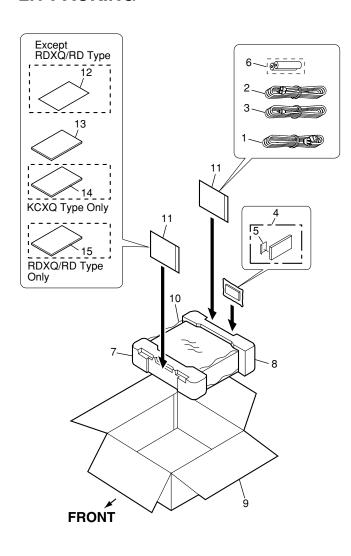
Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

# 2. EXPLODED VIEWS AND PARTS LIST

NOTES: ullet Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List. ullet The ldet mark found on some component parts indicates the importance of the safety factor of the part.

- The ∆ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Screws adjacent to ▼ mark on the product are used for disassembly.

#### 2.1 PACKING



#### (1) PACKING PARTS LIST

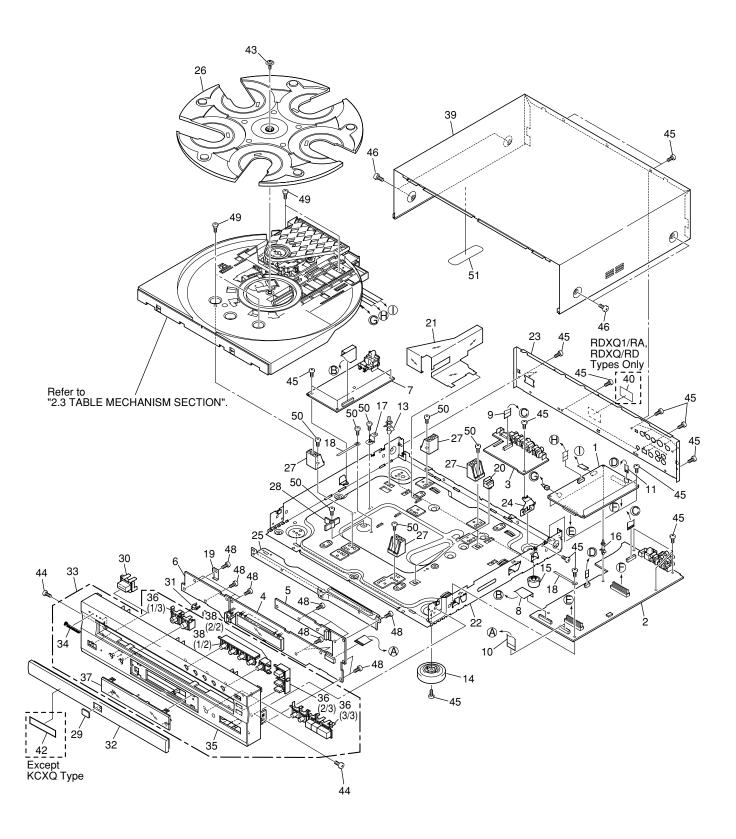
Mark	No.	Description	Part No.
Δ	1	Power Cord	See Contrast table (2)
	2	Audio Cord (L = 1.5m)	VDE1054
	3	Video Cord (L = 1.5m)	VDE1055
	4	Remote Control Unit	VXX2705
	5	Battery Cover	AZA7204
NSP	6	Dry Cell Battery (R6P, AA)	VEM1010
	7	Pad F	VHA1247
	8	Pad R	VHA1248
	9	Packing Case	See Contrast table (2)
	10	Sheet	RHX1006
NSP	11 12 13 14 15	Polyethylene Bag Warranty Card Operating Instructions (English) Operating Instructions (French) Operating Instructions (Spanish/Portuguese)	

#### (2) CONTRAST TABLE

DV-C503/KUXQ, KCXQ, RDXQ1/RA and RDXQ/RD are constructed the same except for the following :

Mark	No.	Symbol and Description	KUXQ Type	KCXQ Type	RDXQ1/RA Type	RDXQ/RD Type	Remarks
∆ NSP	9 12 14	Power Cord Packing Case Warranty Card Operating Instructions (French) Operating Instructions (Spanish/Portuguese)	ADG7022 VHG1895 ARY7045 Not used Not used	ADG7022 VHG1951 ARY7045 VRC1117 Not used	ADG1158 VHG1918 ARY7025 Not used Not used	ADG1158 VHG1919 Not used Not used VRD1110	

#### 2.2 EXTERIOR



# (1) EXTERIOR PARTS LIST

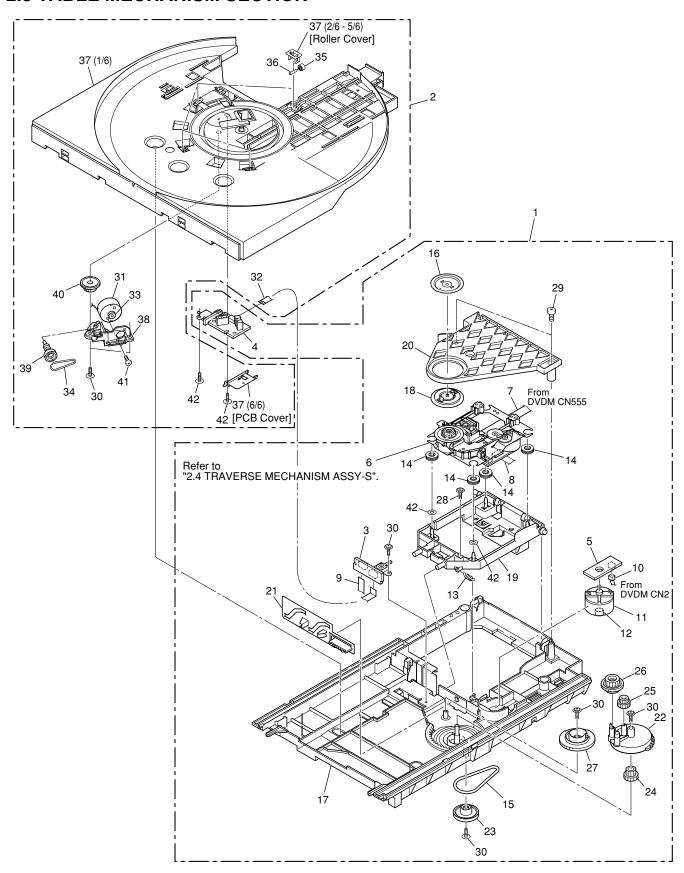
Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	DVDM Assy	VWS1429		26	Rotary Tray	VNK4739
	2	JAC1 Assy	See Contrast table (2)		27	Mechanism Holder	VNL1869
	3	JAC2 Assy	VWV1752		28	Mechanism Support	VNL1906
	4	FLKY Assy	See Contrast table (2)		29	DVD Badge	VAM1102
NSP	5	KYLB Assy	VWG2186		30	PW Button	VNK4101
NSP	6	PWSB Assy	VWG2188		31	LED Lens	VNK4503
$\triangle$	7	POWER SUPPLY Unit	See Contrast table (2)		32	Tray Panel	VNK4604
	8	Flexible Cable (26P)	VDA1825		33	Front Panel Assy	See Contrast table (2)
	9	Flexible Cable (16P)	VDA1826		34	Pioneer Badge	VAM1109
	10	Flexible Cabe (15P)	VDA1827		35	Front Panel	See Contrast table (2)
	11	Flexible Cable (07P)	VDA1828		36	Main Key	VNK4603
	12	••••			37	FL Lens	VNK4617
NSP	13	PCB Holder	PNW2100		38	5 Key	VNK4643
	14	Insulator	PNW2766		39	Bonnet S	VXX2728
	15	Foot Assy	REC1263		40	Label	See Contrast table (2)
NSP	16	PCB Support	REC1285		41	••••	
NSP	17	PCB Base	RNE1221	NSP	42	Getter	See Contrast table (2)
	18	Cord Clamper	RNH-184	NSP	43	TS Screw	DBA1006
	19	Earth Plate	VBK1121		44	Screw	IBZ30P080FMC
	20	PCB Hinge	VEC1174		45	Screw	BBZ30P080FMC
	21	Barrier	VEC2176		46	Screw	BCZ40P060FZK
NSP	22	Base Chassis	VNA2167		47	Screw	IBZ30P080FMC
	23	Rear Panel	See Contrast table (2)		48	Screw	BBZ30P100FZK
NSP	24	JB Stay	VNE2223		49	Screw	VBA1079
	25	FP Angle	VNE2233		50	Screw	BBZ30P060FZK
				NSP	51	Large Label	VRW1855

#### (2) CONTRAST TABLE

DV-C503/KUXQ, KCXQ, RDXQ1/RA and RDXQ/RD are constructed the same except for the following :

Mark	No.	Symbol and Description	KUXQ Type	KCXQ Type	RDXQ1/RA Type	RDXQ/RD Type	Remarks
Δ	4 7 23	JAC1 Assy FLKY Assy POWER SUPPLY Unit Rear Panel Front Panel Assy	VWV1751 VWG2182 VWR1329 VNA2168 VXA2410	VWV1751 VWG2182 VWR1329 VNA2168 VXA2410	VWV1753 VWG2183 VWR1332 VNA2199 VXA2413	VWV1753 VWG2183 VWR1332 VNA2250 VXA2413	
NSP	40	Front Panel Label Getter	VNK4602 Not used VRW1819	VNK4602 Not used Not used	VNK4655 VRW1699 VRW1819	VNK4655 VRW1699 VRW1819	

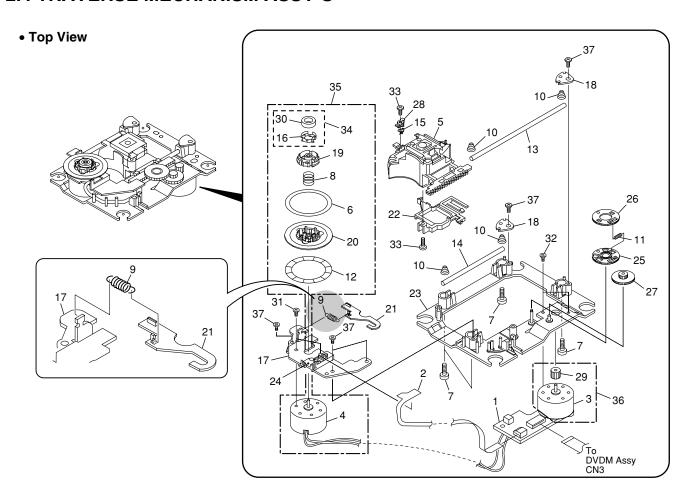
#### 2.3 TABLE MECHANISM SECTION



#### • TABLE MECHANISM SECTION PARTS LIST

Mark	No.	Description	Part No.
NSP	1	,	VWT1173
NSP	2		VWT1175
NSP	3		VWG2179
NSP	4		VWG2180
NSP	5		VWG2181
	6	Traverse Mechanism Assy-S	VXX2653
	7	Flat Flexible Cable (24P)	VDA1836
	8	Flat Flexible Cable (8P)	VDA1837
	9	Flat Flexible Cable (8P)	VDA1838
	10	Housing Assy (2P)	VKP2249
	11 12 13 14 15	Spring	VXM1033 PNW1634 VBH1324 VEB1286 VEB1316
	16 17 18 19 20	Clamper	VNE2162 VNK4647 VNL1738 VNL1907 VNL1872
	21 22 23 24 25	Gear Pulley	VNL1873 VNL1874 VNL1876 VNL1877 VNL1908
	26	Drive Gear	VNL1879
	27	Change Gear	VNL1904
	28	Screw	ABA7009
	29	Screw	BPZ30P080FMC
	30	Screw	Z39-019
	31 32 33 34 35	Motor Pulley	DXM1118 VDA1835 PNW1634 VEB1317 VEB1318
	36	Roller Shaft	VLL1511
	37	Slide Table	VNK4751
	38	Motor Holder	VNL1880
	39	Worm Gear	VNL1881
	40	Tray Gear	VNL1882
	41	Screw	JGZ17P028FMC
	42	Spacer	VEC2177

#### 2.4 TRAVERSE MECHANISM ASSY-S

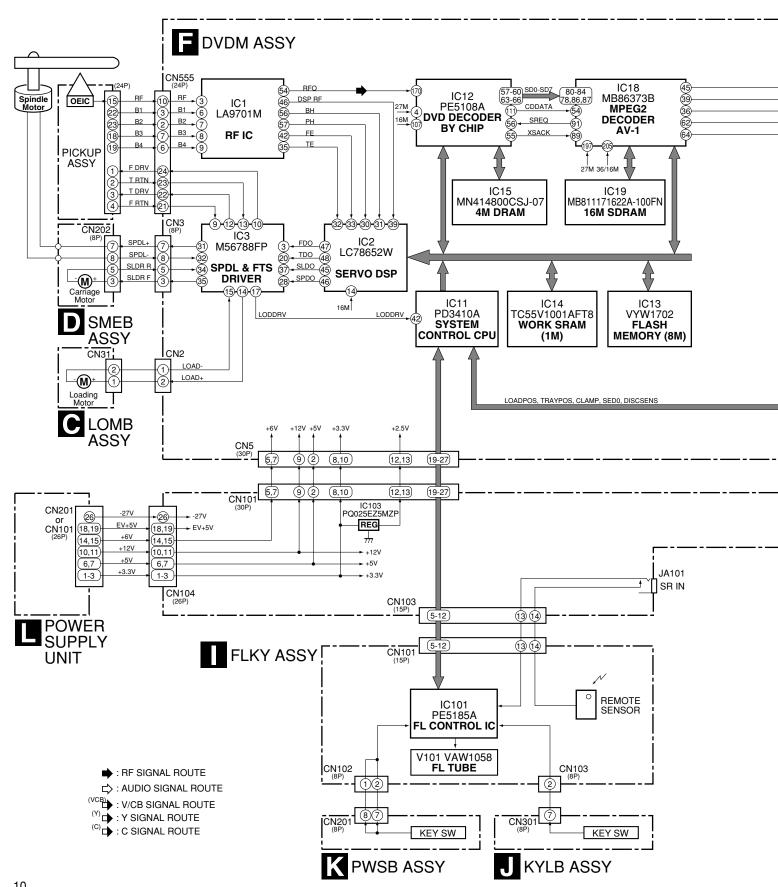


#### • TRAVERSE MECHANISM ASSY-S PARTS LIST

Mark N	lo.	Description	Part No.	<u>Mark</u>	No.	Description	Part No.
NSP	1	SMEB Assy	VWG2048		21	Hook	VNL1770
NSP	2	FGSB Assy	VWG2009		22	FFC Holder	VNL1802
NSP	3	Motor (CARRIAGE)	VXM1079		23	Mechanism Base	VNL1806
NSP	4	Motor (SPINDLE)	VXM1084		24	FG Holder	VNL1807
⚠ NSP	5	Pickup Assy	VWY1055		25	Gear A	VNL1808
	6	Table Sheet	DEC2040		26	Gear B	VNL1809
	7	Screw	VBA1058		27	Gear C	VNL1810
	8	Centering Spring	VBH1278		28	Slider	VNL1811
	9	Hook Spring	VBH1317		29	Gear D	VNL1814
	10	Skew Spring	VBH1303	NSP	30	Magnet	VYM1024
	11	Gear Spring	VBH1308		31	Screw	JFZ17P025FZK
NSP	12	Reflected Sheet	VEC1959		32	Screw	JGZ17P028FMC
	13	Guide Bar	VLL1504		33	Screw	VBA1051
	14	Sub-guide Bar	VLL1505		34	Magnet Holder Assy	VXX2507
	15	Hold Spring	VNC1017		35	Spindle Motor Assy	VXX2649
NSP	16	Magnet Holder	VNE2070		36	Carriage Motor Assy	VXX2650
NSP	17	Motor Base	VNE2154		37	Screw	PBA1069
NSP	18	Cover	VNE2155				
	19	Centering Ring	VNL1746				
NSP	20	Disc Table	VNL1747				

# 3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM

#### 3.1 BLOCK DIAGRAM



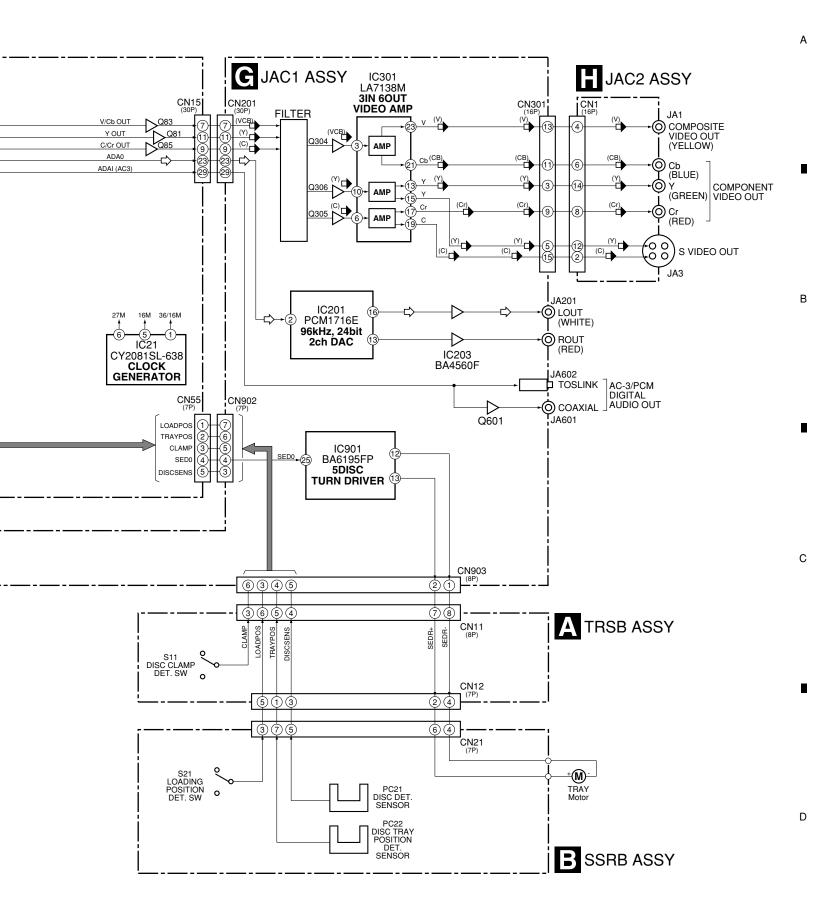
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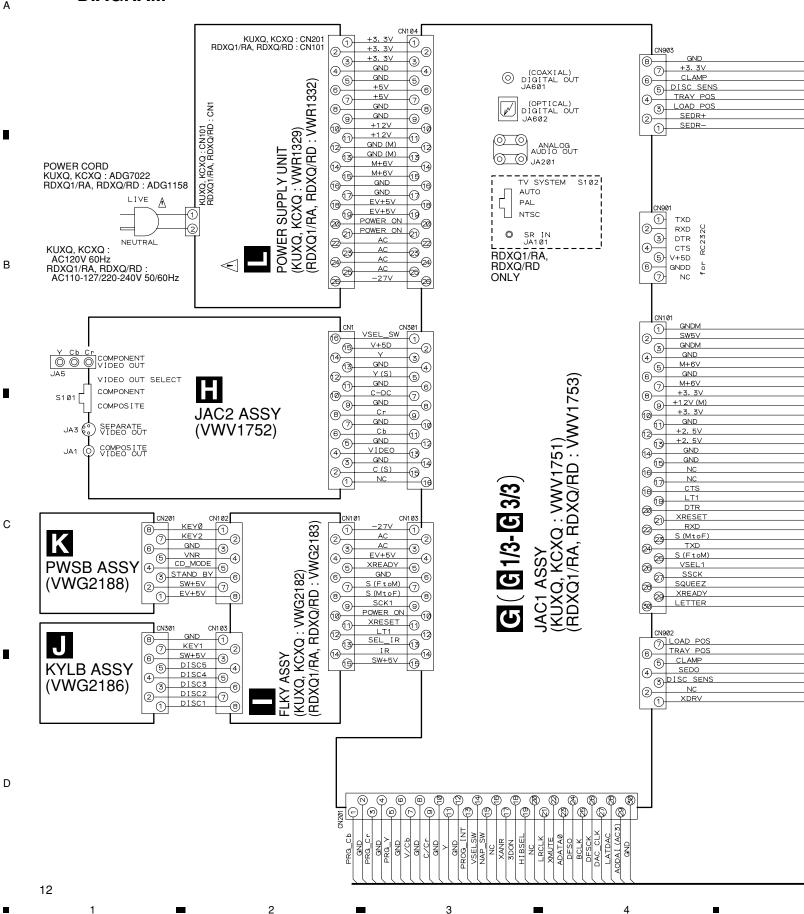


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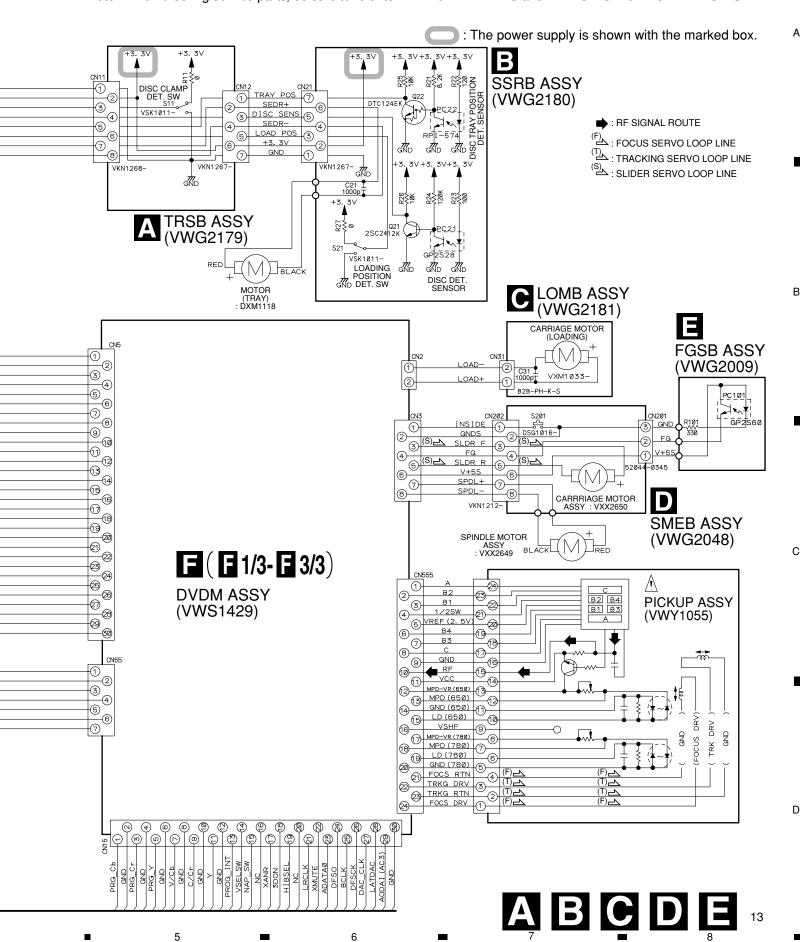
#### 3.2 TRSB, SSRB, LOMB, SMEB, FGSB ASSYS and OVERALL WIRING DIAGRAM

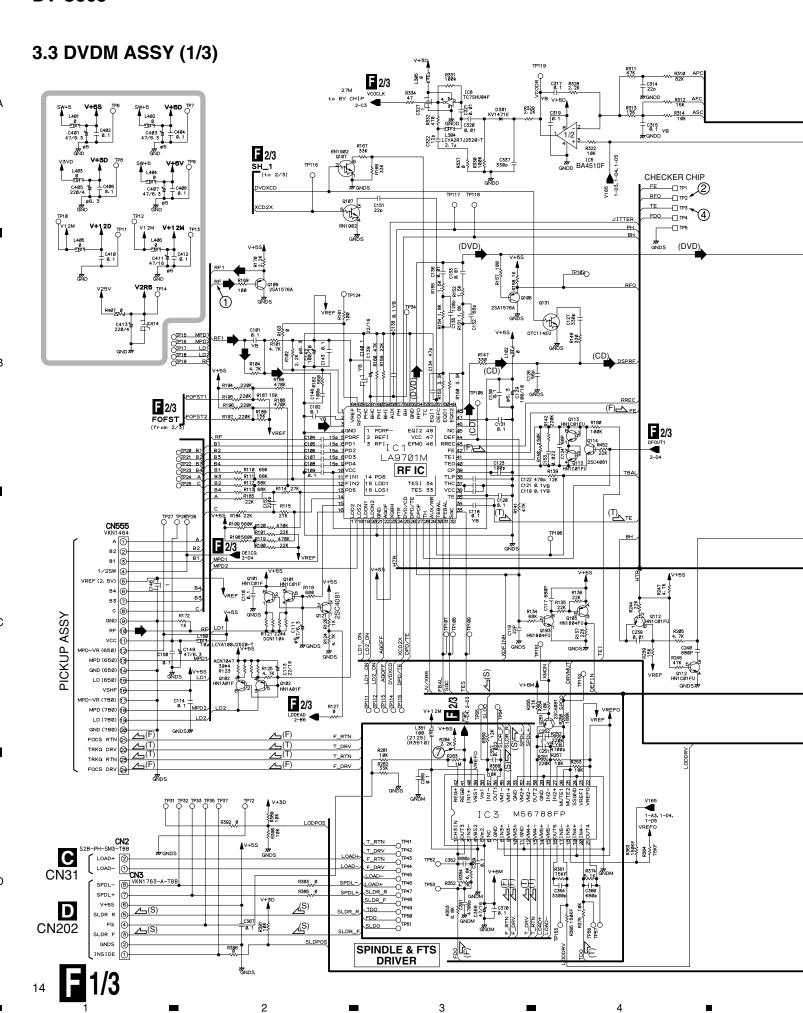
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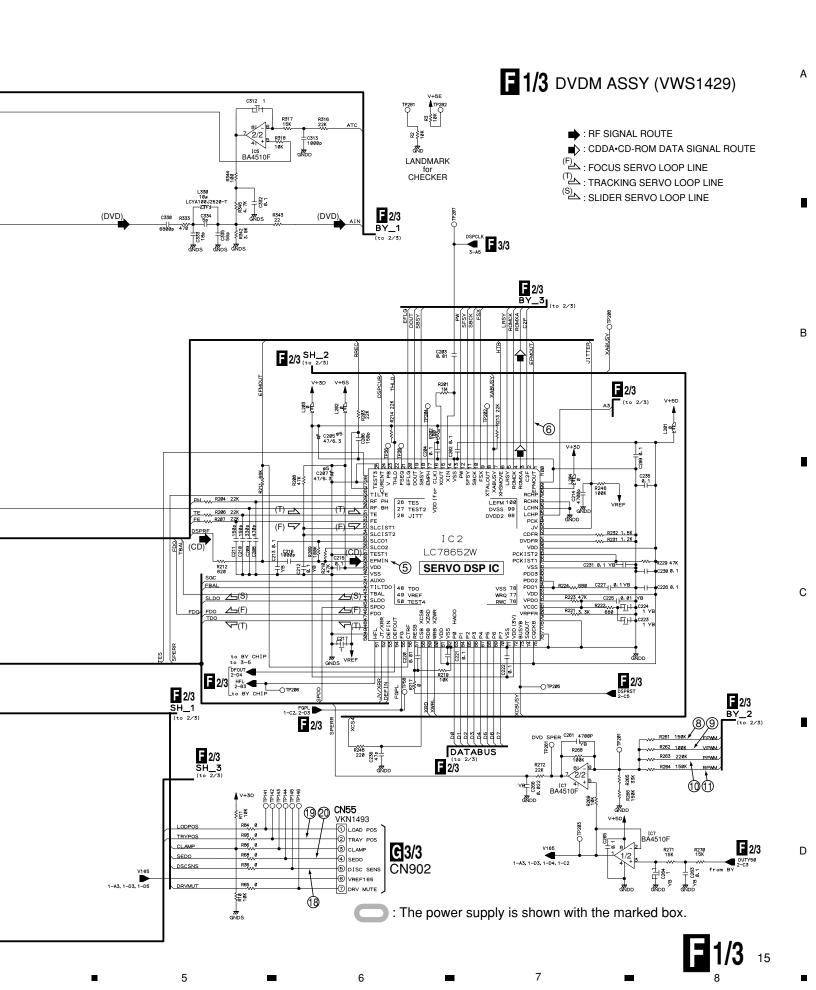
2



Note: When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".



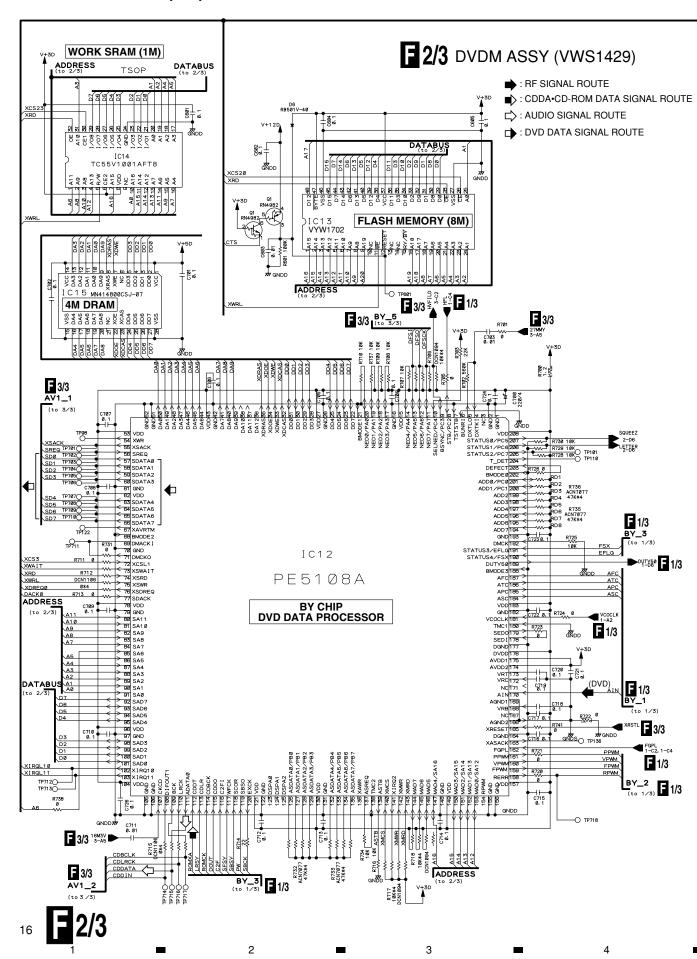




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#### 3.4 DVDM ASSY (2/3)

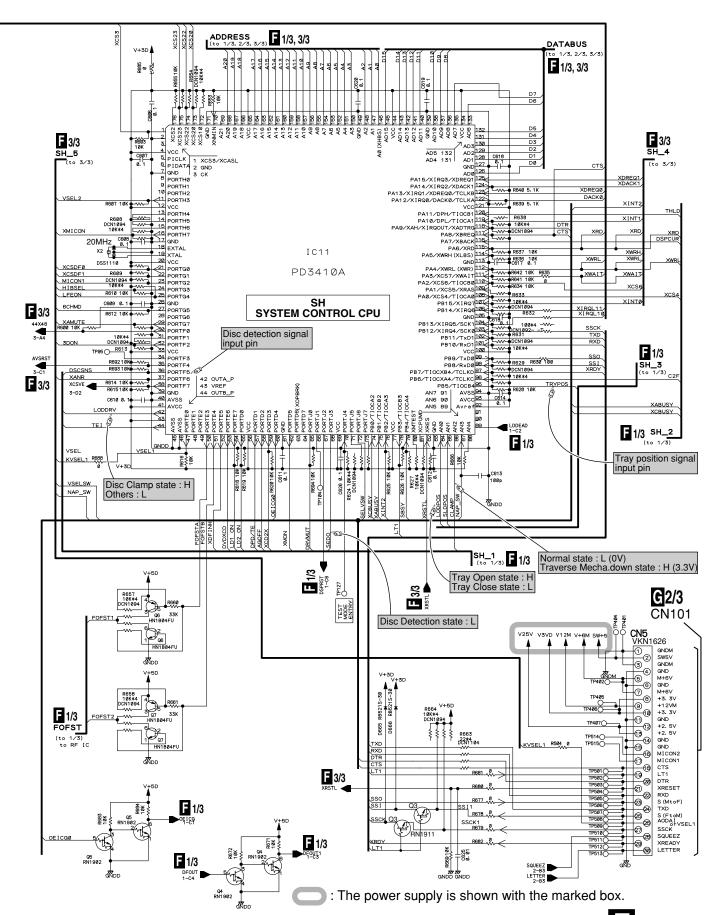


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**2/3** <sub>1</sub>

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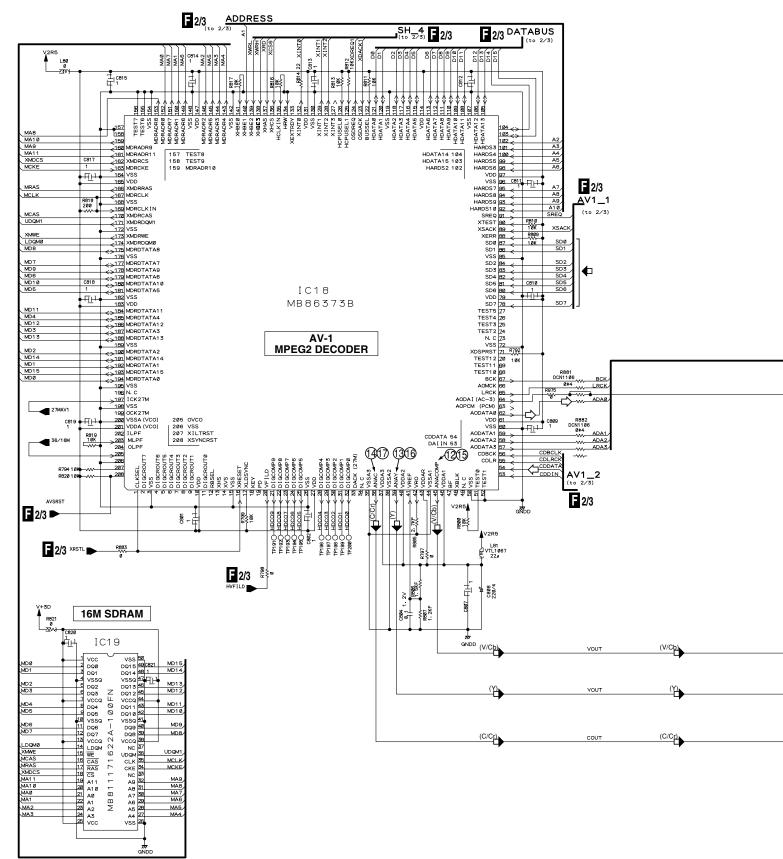
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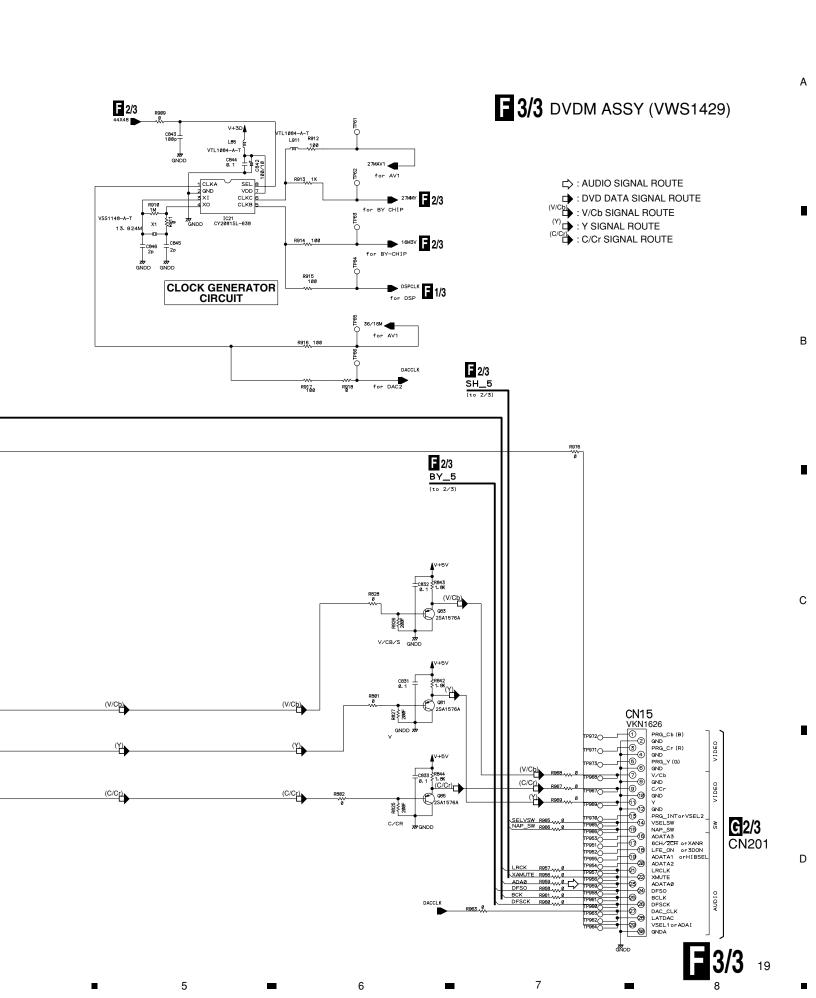
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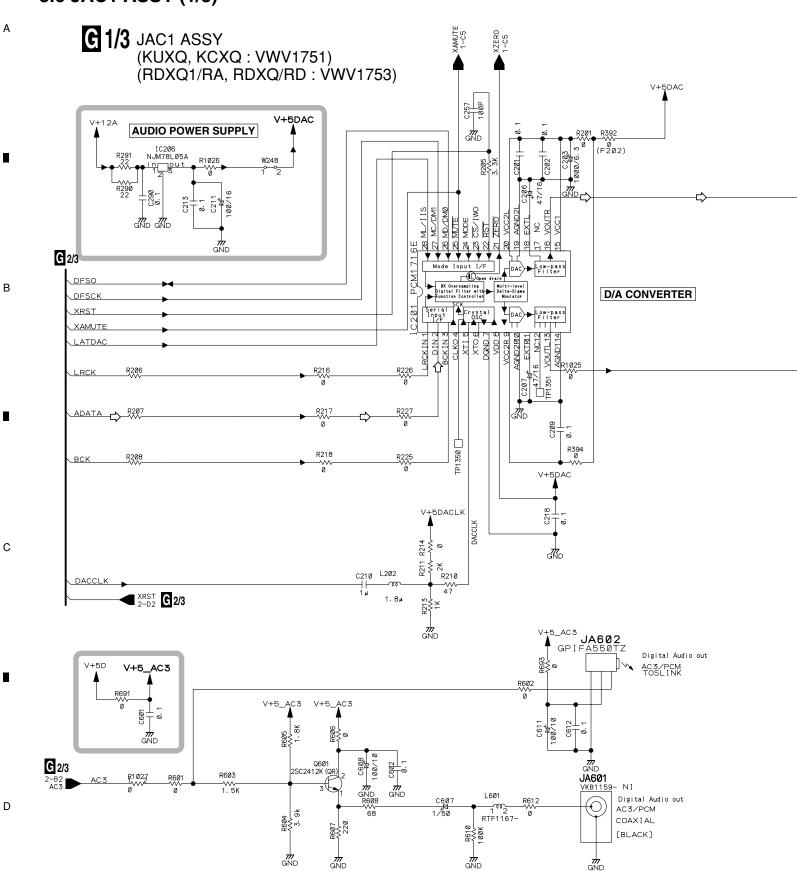
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# 3.6 JAC1 ASSY (1/3)

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**G** 1/3

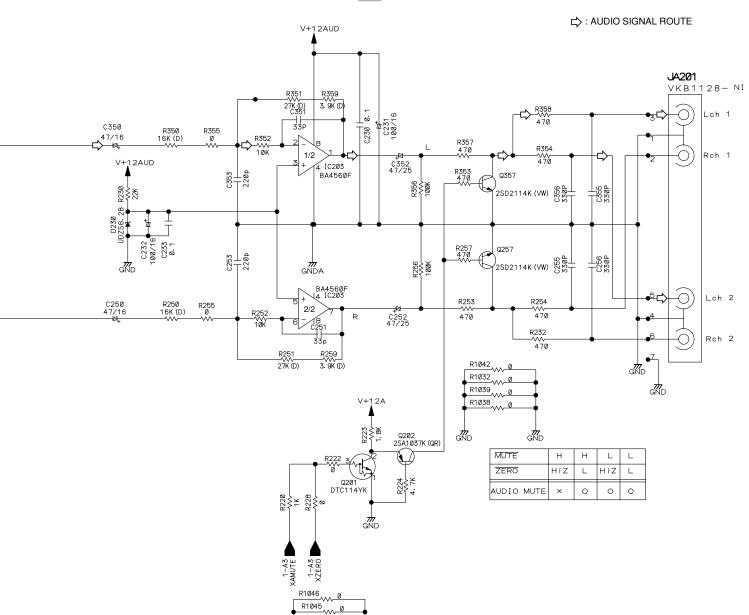
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: The power supply is shown with the marked box.

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aND

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gND.

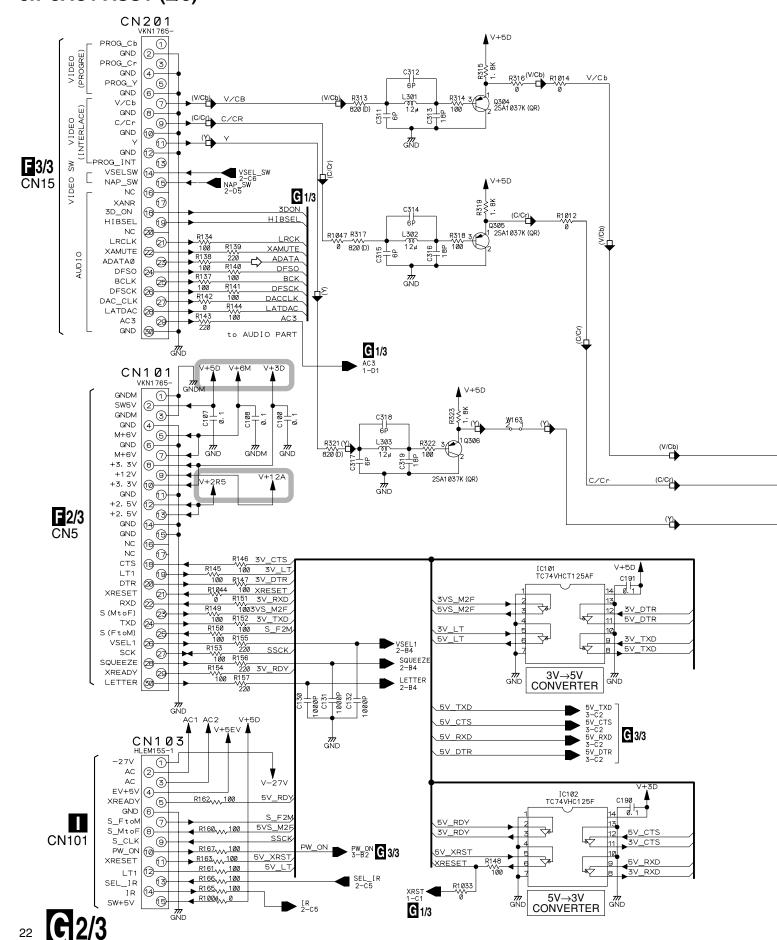
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## 3.7 JAC1 ASSY (2/3)

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8 **DV-C503** 

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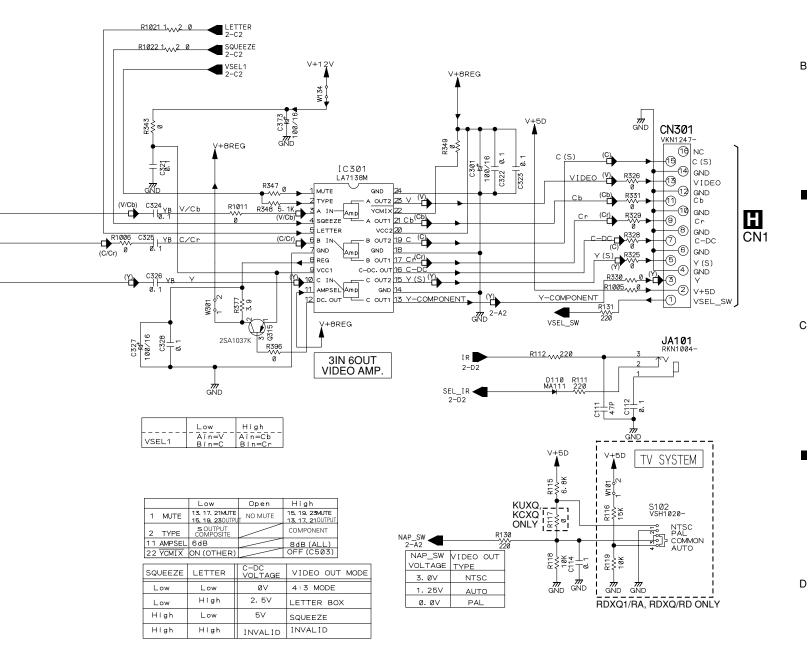
G 2/3 JAC1 ASSY (KUXQ, KCXQ: VWV1751) (RDXQ1/RA, RDXQ/RD: VWV1753)

6

: The power supply is shown with the marked box.

: V/Cb SIGNAL ROUTE (Y) : Y SIGNAL ROUTE (C/Cr) : C/Cr SIGNAL ROUTE

7

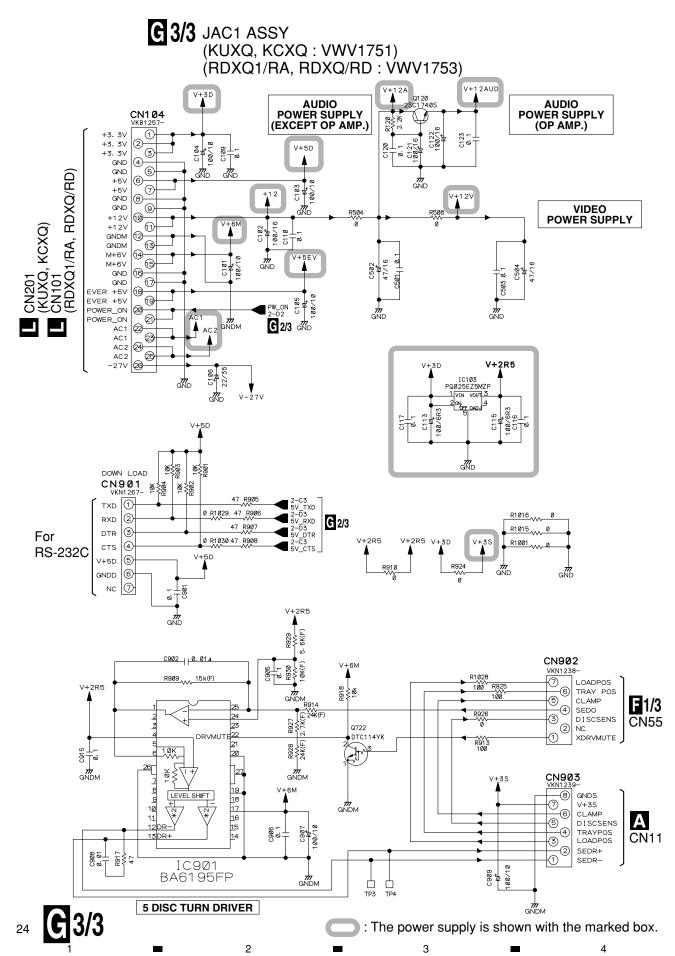


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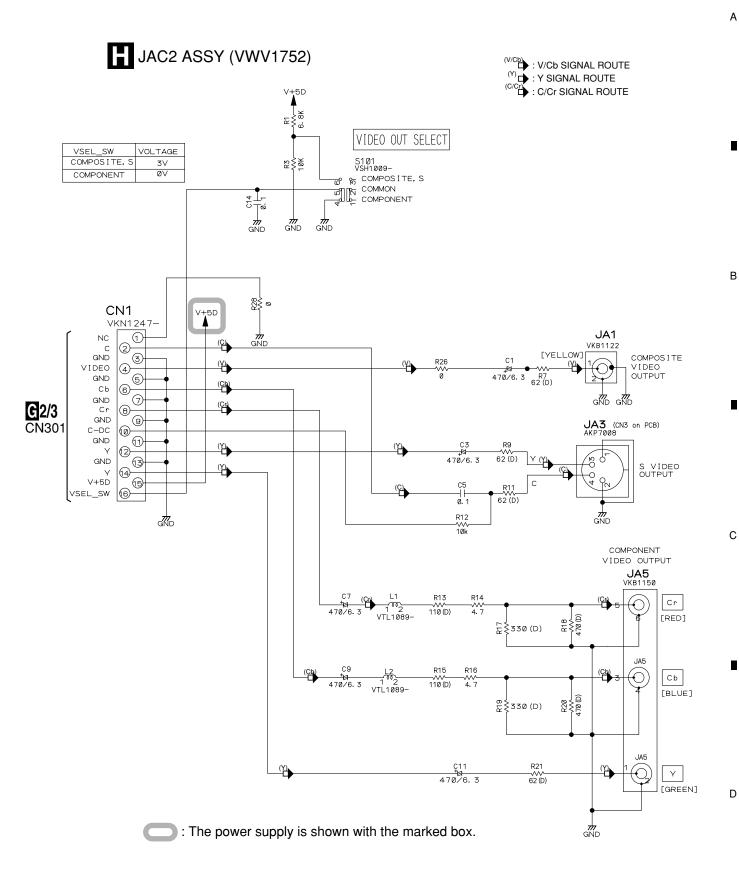
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## 3.8 JAC1 ASSY (3/3)

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# 3.9 JAC2 ASSY

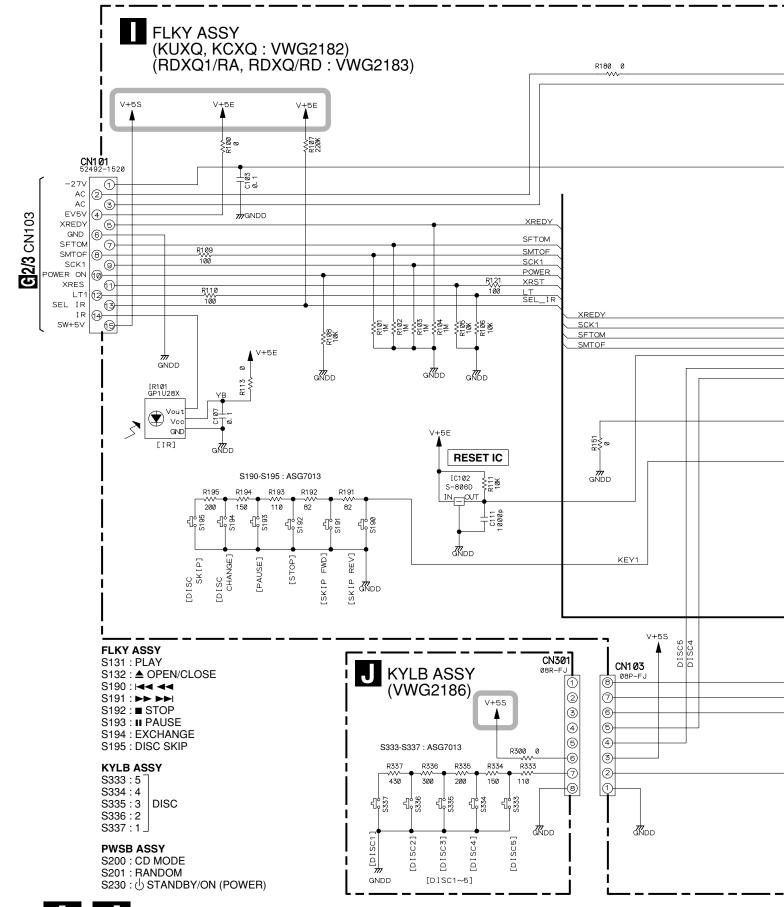


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#### 3.10 FLKY, KYLB and PWSB ASSYS

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: The power supply is shown with the marked box. V 1 0 1 V A W 1 0 5 8 - A FL TUBE 10 11 12 1314 15 16 17 18 19 20 2122 23 24 25 26 27 28 29 30 31 32 33 34 V+5E V+5E P12 P13 64 P14 62 G5 IC101 P15 61 G4 PE5185A G3 d GNDD C186 5 **FL CONTROL** G2 6 7 MICROCOMPUTER VNR VCC 8 CD MODE FL SET 1 LED (CD MODE) 9 10 FL SET 2 LED (DVD) R170 100 112 XREADY R171 100 114 SCK1 OEM SELECT В STANDBY GNDD GNDD LED (STANDBY) R157 VDD LED (DISC1) DISC1 LED (DISC2) 50 DISC2 DVD MAIN DISC3 LED (DISC3) 49 977 GNDD SI1 715 RESET IN
17 RESET IN
18 LED (DISC5)
19 VSS IC 48 SEL SEL IR LT V+5E ↓ ON POWER TES 44 21 R173\_\_\_\_\_100 XRST ≥23 REGION RESET OUT POWER 24 KEY2 POWER ON 577 GNDD d GNDD 3|29|30|31|32|33|34|35|36|37|38|39|40 777 GNDD 577 GNDD KEY0 777 GNDD V+5E V+5E SS S 加 GNDD ⊥2.% ∏54-₹ 24 24 74 74 IC101 is damaged → No Power ON ₩ GNDD ₹ 5 5 5 5 5 4 KUXQ, KCXQ :  $36k\Omega$ RDXQ1/RA, RDXQ/RD :  $68k\Omega$ KUXQ, KCXQ : 6.2kΩ RDXQ1/RA, RDXQ/RD : 27kΩ D1SC1 D1SC2 D1SC3 N. V+5S V+5E -(8) 10 V+5S 7 2 STANDBY 6 3 CD\_MODE 7256 188 188 (5) 4 VNR 4 (5) 3 6 KEY2 2 7 R132 R131 82 KEY0 ASG7013 (1) 8 ASG7013 MODE] CN102 CN201 ₹252 S132 ASG7013 ASG7013 /CLOSE] D [PLAY] DTC124EK gNDD gNDD 777 GNDD GNDD **PWSB ASSY** 777 GNDD (VWG2188)

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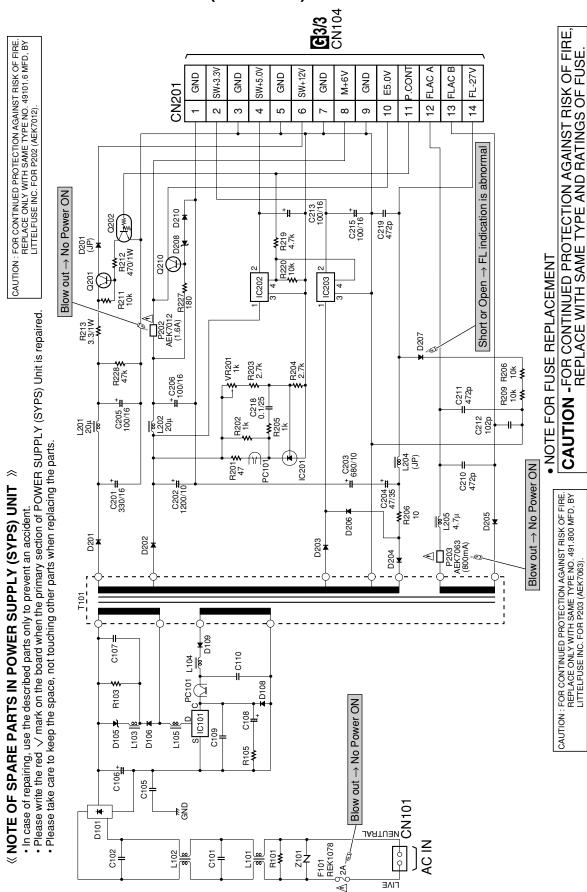
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#### 3.11 POWER SUPPLY UNIT (VWR1329)

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POWER SUPPLY UNIT (VWR1329)

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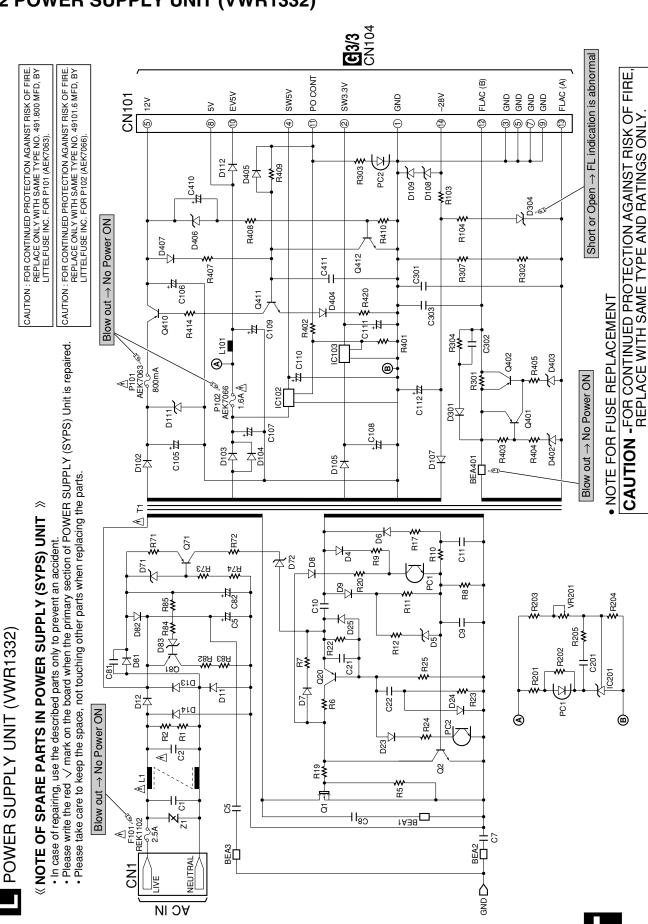
4

# 3.12 POWER SUPPLY UNIT (VWR1332)

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2

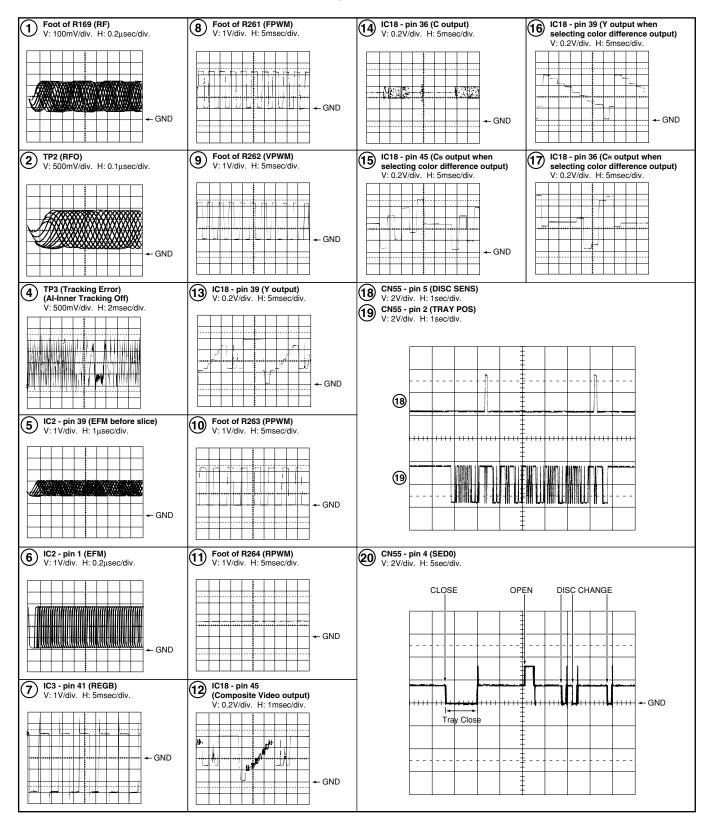
1

#### WAVEFORMS

Note: The encircled numbers denote measuring point in the schematic diagram.

Measurement condition: No. 1 to 4 and 6 to 11 : MJK1, Title 1-chp 1

No. 5 : CD, ABEX-784 Track 1 No. 12 to 14 : MJK1, Title 1-chp 4 No. 15 to 17 : MJK1, Title 1-chp 5



# 4. PCB CONNECTION DIAGRAM

#### **NOTE FOR PCB DIAGRAMS:**

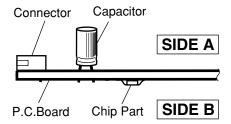
- 1. Part numbers in PCB diagrams match those in the schematic
- 2. A comparison between the main parts of PCB and schematic diagrams is shown below.

Symbol In PCB Diagrams	Symbol In Schematic Diagrams	Part Name
<b>© 0 0</b> B C E		Transistor
• <u>© 0 0</u> B C E		Transistor with resistor
<b>© 0 0</b> D G S		Field effect transistor
@00 <u>%000</u> X	***************************************	Resistor array
000		3-terminal regulator

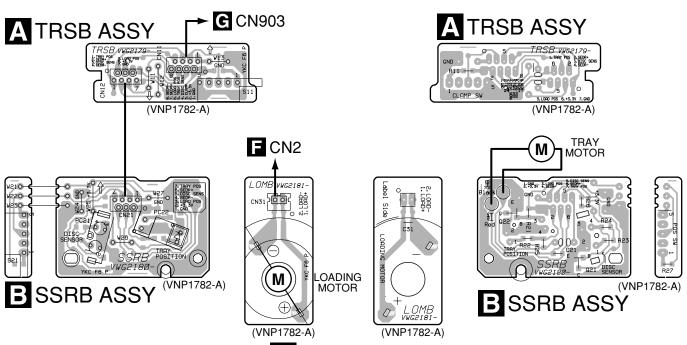
3. The parts mounted on this PCB include all necessary parts for several destinations.

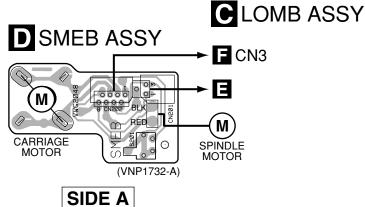
3

- For further information for respective destinations, be sure to check with the schematic diagram.
- 4. View point of PCB diagrams.



# 4.1 TRSB, SSRB, LOMB and SMEB ASSYS





2

SIDE B **A B C D** 31

**DV-C503** 

#### **4.2 DVDM ASSY**

F DVDM ASSY • This PCB is a four-layered board. HCL12494V-Ø HEAT-RESISTANCE PRI-FLUX C4Ø1 Q3 IC7 IC5 IC8 IC11 IC15 Q121 Q101 Q7 Q6 IC21 Q4 Q103 PICKUP ASSY Q5 Q102 IC18 IC1 Q109 Q107 Q81 IC3 Q91 Q83 (VNP1776-A) **G** CN902

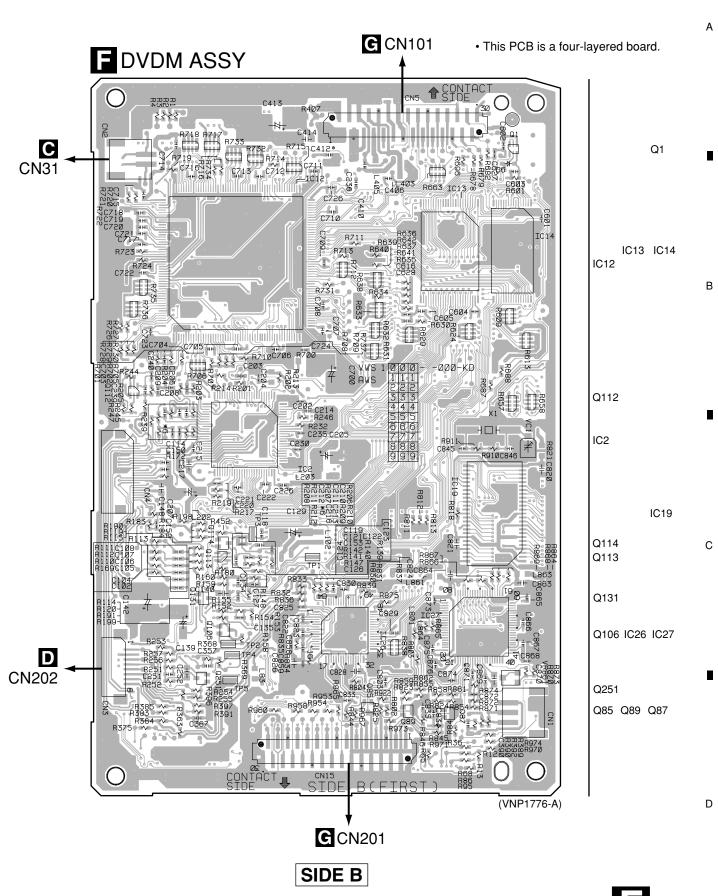
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2

3

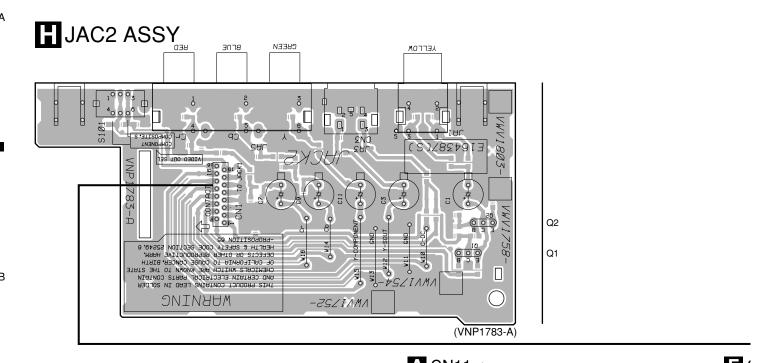
SIDE A

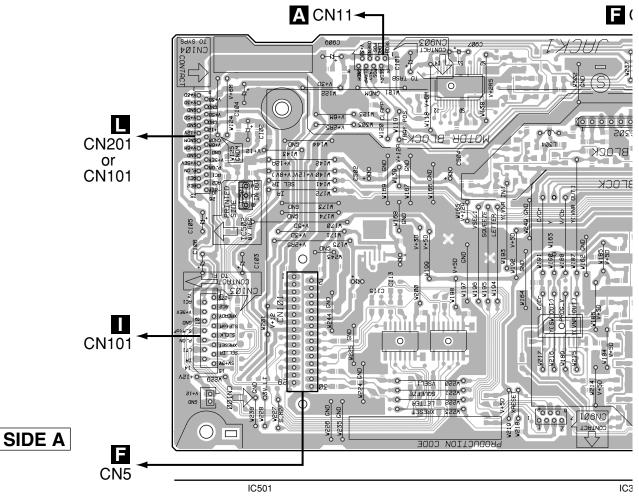
DV-C503



3:

#### 4.3 JAC1 and JAC2 ASSYS





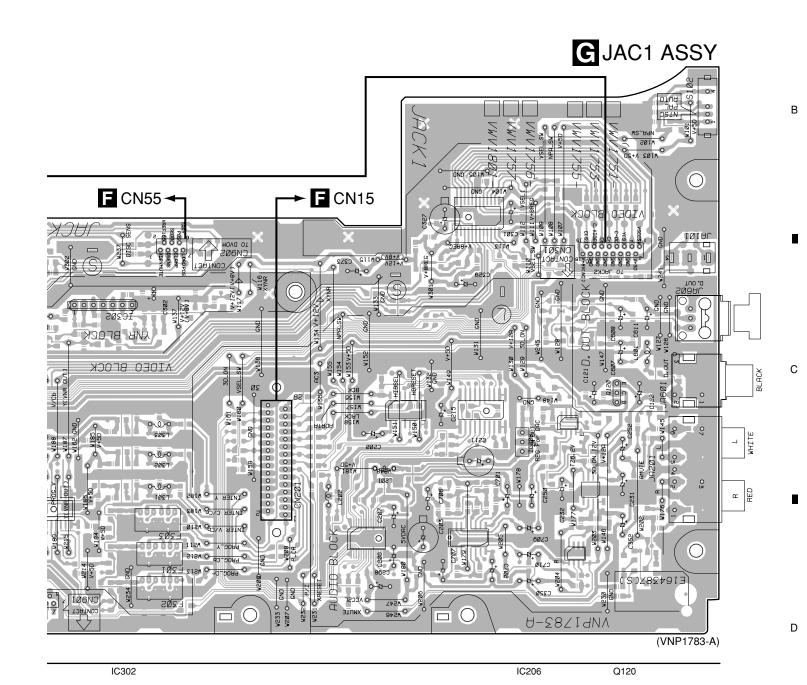


2

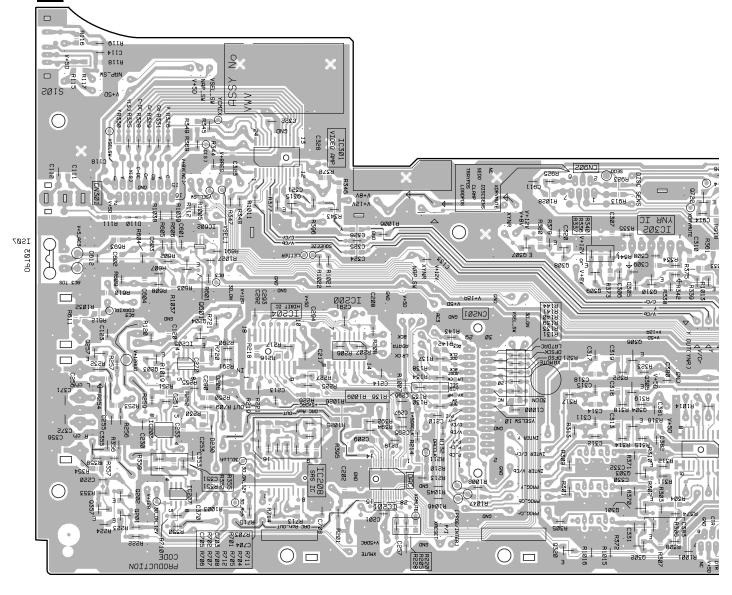
3

**=** 4

DV-C503



G JAC1 ASSY

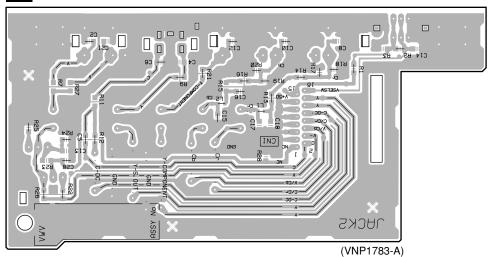


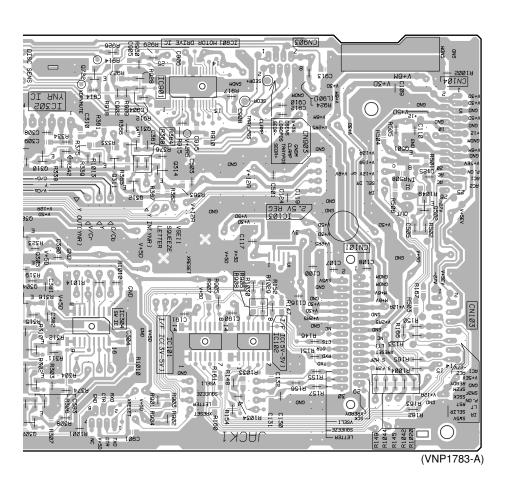
Q307-Q310 Q301-Q306 Q257 Q203 IC602 IC301 IC200 Q722 Q315 Q601 IC204 IC304 IC203 Q204 IC205 IC207 IC201 Q202 Q357 IC208 Q201

**DV-C503** 

# JAC2 ASSY

5





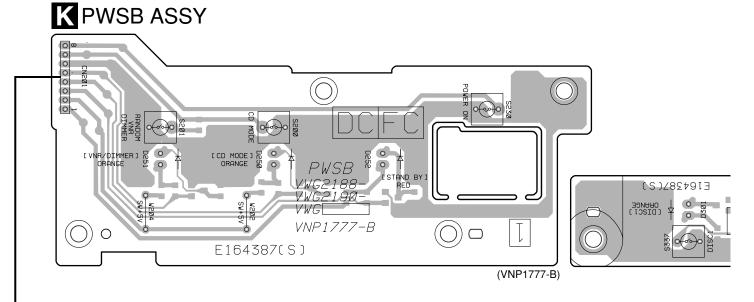
SIDE B

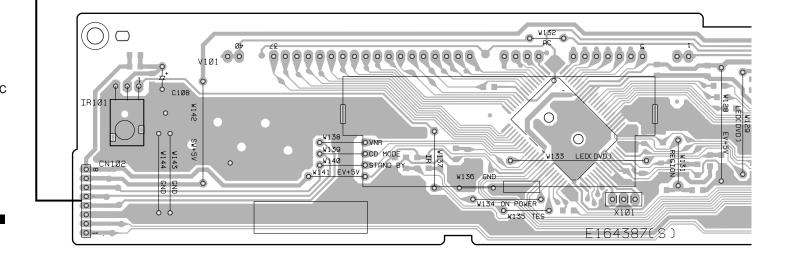
Q722 IC901 Q502 Q311-Q314 Q306 IC304 IC101 IC102

С

5



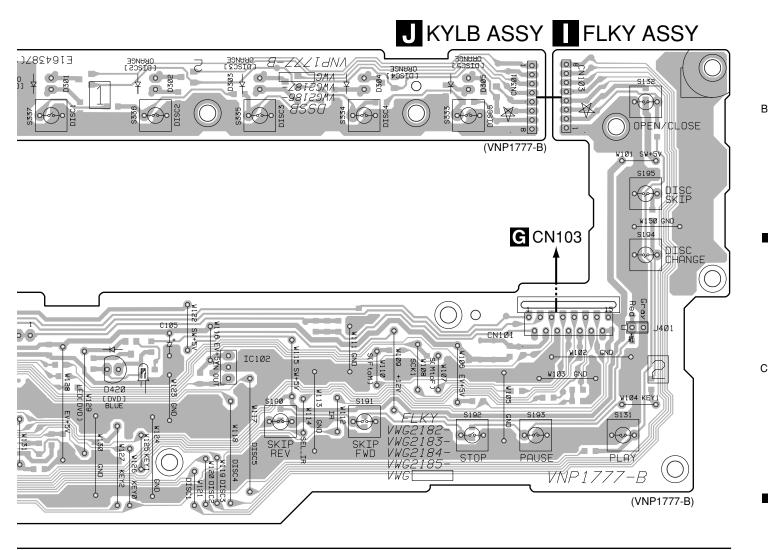




I J K

**DV-C503** 

В



IC102

5

SIDE A



D

5

D

2

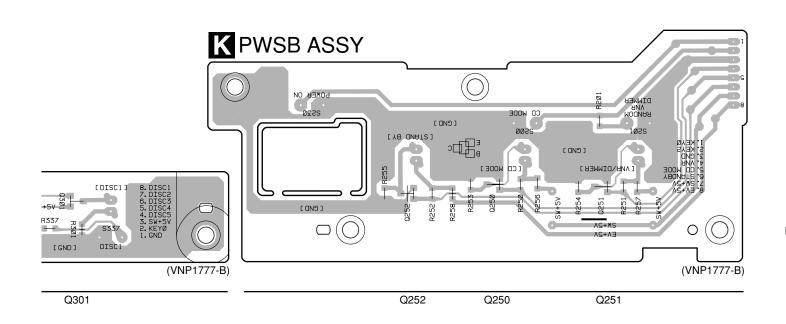
FLKY ASSY J KYLB ASSY [DISC1 S337 DISC1 Q301 Q305 Q304 Q303 Q302 DISC CHANGE Contact Side 接触面 R121 [ GND ] [ GND ]

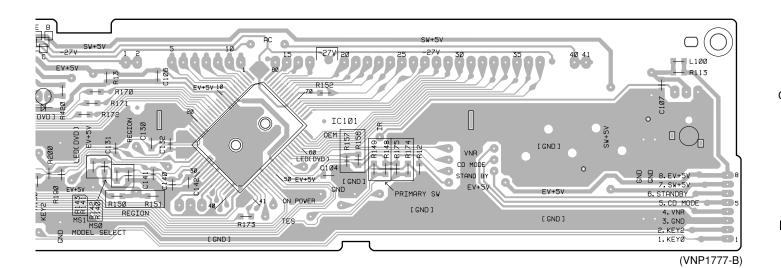
3

3

Q420

**DV-C503** 





20 IC101

5

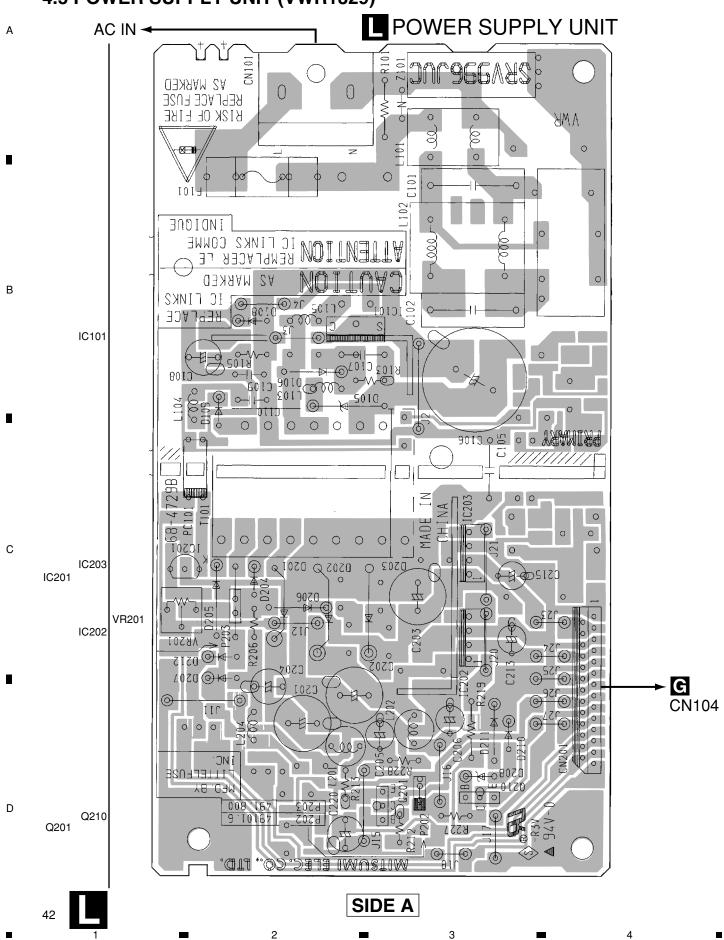
SIDE B



D

**DV-C503** 

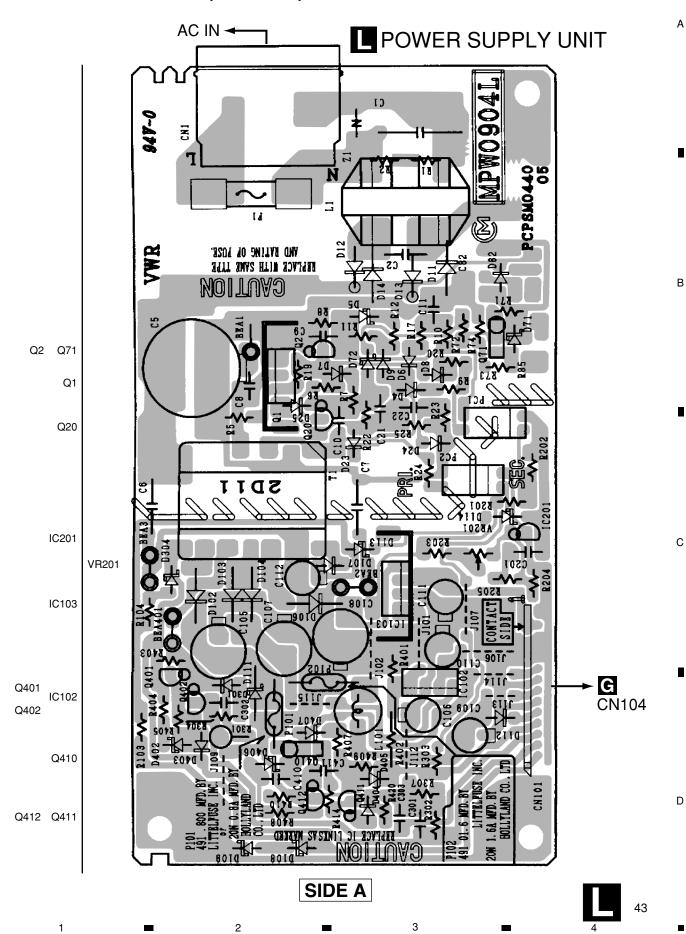
4.5 POWER SUPPLY UNIT (VWR1329)



В

# 4.6 POWER SUPPLY UNIT (VWR1332)

2



# 5. PCB PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- The  $\triangle$  mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples.
   Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

### **■LIST OF WHOLE PCB ASSEMBLIES**

Mark	Symbol and Description Part No.					
IVIAIK	Symbol and Description	KUXQ Type	KCXQ Type	RDXQ1/RA Type	RDXQ/RD Type	Remarks
NSP	5C Mechanism Base Assy	VWT1173	VWT1173	VWT1173	VWT1173	
NSP	⊢ MECB Assy	VWM1992	VWM1992	VWM1992	VWM1992	
NSP	├─ TRSB Assy	VWG2179	VWG2179	VWG2179	VWG2179	
NSP	SSRB Assy	VWG2180	VWG2180	VWG2180	VWG2180	
NSP	LOMB Assy	VWG2181	VWG2181	VWG2181	VWG2181	
NSP	☐ Traverse Mechanism Assy	VWT1161	VWT1161	VWT1161	VWT1161	
NSP	SMEB Assy	VWG2048	VWG2048	VWG2048	VWG2048	
NSP	└─ FGSB Assy	VWG2009	VWG2009	VWG2009	VWG2009	
	DVDM Assy	VWS1429	VWS1429	VWS1429	VWS1429	
NSP	JKSB Assy	VWM2028	VWM2028	VWM2029	VWM2029	
	├─ JAC1 Assy	VWV1751	VWV1751	VWV1753	VWV1753	
	└─ JAC2 Assy	VWV1752	VWV1752	VWV1752	VWV1752	
NSP	FLKB Assy	VWM1995	VWM1995	VWM1996	VWM1996	
	FLKY Ássy	VWG2182	VWG2182	VWG2183	VWG2183	
NSP	KYLB Assy	VWG2186	VWG2186	VWG2186	VWG2186	
NSP	PWSB Assy	VWG2188	VWG2188	VWG2188	VWG2188	
Δ	POWER SUPPLY Unit	VWR1329	VWR1329	VWR1332	VWR1332	

# G JAC1 ASSY

VWV1751 and VWV1753 are constructed the same except for the following:

Marile	Complete and Description	Part	Domonico	
Mark	Symbol and Description	VWV1751	VWV1753	Remarks
	S102	Not used	VSH1020	
	R116	Not used	RS1/10S153J	
	R117	RS1/10S0R0J	Not used	
	R119	Not used	RS1/10S103J	

# FLKY ASSY

VWG2182 and VWG2183 are constructed the same except for the following:

Marile	Complete and Description	Part	Part No.			
Mark	Symbol and Description	VWG2182	VWG2183	Remarks		
	R141 R143	RS1/10S622J RS1/10S363J	RS1/10S273J RS1/10S683J			

# ■ PCB PARTS LIST FOR DV-C503/KUXQ UNLESS OTHERWISE NOTED

Mark No.	Description	Part No.	Mark No. Description	Part No.
<b>A</b> TRSB	ASSY		FGSB ASSY	
SWITCH			SEMICONDUCTOR	
S11		VSK1011	PC101	GP2S60
RESISTOR			RESISTORS	
R11		RS1/10S0R0J	R101	RS1/10S331J
OTHERS				
CN12 CN11	8P FFC CONNECTOR 7P FFC CONNECTOR	VKN1267 VKN1268	DVDM ASSY	
			SEMICONDUCTORS	
E copp	ACCV		IC5,IC7 IC21	BA4510F CY2081SL-638
<b>S</b> SRB	ASSY		IC1	LA9701M
SEMICONDU	JCTORS		IC2 IC3	LC78652W M56788FP
Q21 Q22		2SC2412K DTC124EK	IC19	MB811171622A-100FN
PC21		GP2S28	IC19 IC18	MB86373B
PC22		RPI-574	IC15 IC11	MN414800CSJ-07 PD3410A
SWITCH			IC12	PE5108A
S21		VSK1011	IC14	TC55V1001AFT8
CAPACITOR	•		IC8	TC7SHU04F
CAPACITOR C21	L	CKSQYF102Z50	IC13 Q106,Q109,Q81,Q83,Q85	VYW1702 2SA1576A
			Q114,Q121,Q251	2SC4081
RESISTORS All Resi		RS1/10S□□□J	Q131	DTC114EUA
7 1 100.	0.0.0		Q102 Q103,Q6,Q7	HN1A01F HN1B04FU
OTHERS			Q101	HN1C01F
CN21	7P FFC CONNECTOR	VKN1267	Q112,Q113	HN1C01FU
			Q107,Q4,Q5 Q3	RN1902 RN1911
<b>C</b> LOMB	ACCV		Q1	RN4982
			D301 D6	KV1471E RB501V-40
CAPACITOR C31		CKSQYF102Z50	Dece Dece	DDE010 00
U31		CKSQ1F102250	D665,D666	RB521S-30
OTHERS			COILS	
CN31	KR CONNECTOR	B2B-PH-K-S	L150,L330 L304	LCYA100J2520 LCYA2R7J2520
			L81 CHIP COIL	VTL1067
<b>5</b>			L85,L911 CHIP BEAD	VTL1084
D SMEB	ASSY		CAPACITORS	
SWITCH			C123,C146,C613,C843	CCSRCH101J50
S201		DSG1016	C322 C135	CCSRCH120J50 CCSRCH121J50
OTHERS			C104-C108	CCSRCH150J50
CN201	3P FFC CONNECTOR	52044-0345	C206,C210,C211	CCSRCH151J50
CN202	8P FFC CONNECTOR PC BOARD SMEB	VKN1212 VNP1732	C333	CCSRCH180J50
	I O DOM ID SIVILD	VINI 1/JZ	C116,C151,C314 C152	CCSRCH220J50 CCSRCH221J50
			C127,C209,C337	CCSRCH331J50
			C134,C236	CCSRCH470J50

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	C122,C	208	CCSRCH471J50		R361,F	R364	RS1/16S7502F
	C126,C	335	CCSRCH560J50		Other I	Resistors	RS1/16S□□□J
	C334	100	CCSRCH5R0C50				
	C124,C	132 240,C352,C360	CCSRCH680J50 CCSRCH681J25	OTHE	RS		
	C117,C	240,0352,0360	CC3hCn661J25		X2	CHIP CERALOCK	DSS1110
	C845,C	846	CCSRCK2R0C50			(20MHz)	
		142,C842	CEV101M10		CN2	PH CONNECTOR	S2B-PH-SM3
	C113,C		CEV220M16		ONEEE	FLEXIBLE CABLE (07P)	VDA1681 VKN1464
		413,C700,C808	CEV221M4		CN555	24P FFC CONNECTOR	VNN 1404
	C111,C	149,C205,C207,C401	CEV470M6R3		CN55	07P FFC CONNECTOR	VKN1493
	C403.C	407	CEV470M6R3		CN15,		VKN1626
		223,C224,C252,C264	CKSQYB105K10			B TO B CONNECTOR 30	
	C312		CKSQYB105K10		CN3	8P FFC CONNECTOR	VKN1763
		217,C327,C414	CKSQYF105Z16		X1	CRYSTAL RESONATOR (13.824MHz)	VSS1148
	C801,C	802,C807,C809-C815	CKSQYF105Z16			(10.024111112)	
	C817-C	821	CKSQYF105Z16				
	C216,C		CKSRYB102K50				
		136,C203,C220,C225	CKSRYB103K50	G.	JAC1	ASSY	
		320,C321,C603,C625	CKSRYB103K50				
	C703,C	711	CKSRYB103K50	SEMI	COND	UCTORS	
	C101 C	102,C114,C118,C119	CKSRYB104K16		IC203		BA4560F
		138,C204,C212,C213	CKSRYB104K16		IC901		BA6195FP
		231,C263,C315,C317	CKSRYB104K16		IC301 IC206		LA7138M NJM78L05A
	C332,C		CKSRYB104K16		IC200		PCM1716E
	C153,C	266	CKSRYB223K25		10201		1 OM17 TOL
	0057		OKODYDOOKEO		IC103		PQ025EZ5MZP
	C357 C354		CKSRYB223K50 CKSRYB332K50		IC102		TC74VHC125F
		251,C261,C351	CKSRYB472K50		IC101	2004 2000 2015	TC74VHCT125AF
	C330		CKSRYB682K50		Q202,0 Q120	Q304-Q306,Q315	2SA1037K 2SC1740S
	C109,C	110,C120,C130,C131	CKSRYF104Z16		Q120		23017403
	04400	150 0000 0015	01/05//5101710		Q601		2SC2412K
		150,C202,C215 222,C226,C230,C235	CKSRYF104Z16 CKSRYF104Z16		Q257,0		2SD2114K
		299,C319,C359,C367	CKSRYF104Z16		Q201,0	Q722	DTC114YK
		370,C402,C404,C406	CKSRYF104Z16		D110 D230		MA111 UDZS6.2B
		410,C412,C601,C602	CKSRYF104Z16		D230		00230.20
				COIL	2		
		612,C614,C615	CKSRYF104Z16	COIL		202	L ALI400 L TA
		620,C626,C701,C702 710,C712-C724,C726	CKSRYF104Z16 CKSRYF104Z16		L301-L L202	.303	LAU120J-TA LAU1R8J-TA
		833,C844	CKSRYF104Z16		L601	NOISE FILTER	RTF1167
	C112		CKSRYF105Z10				
				CAPA	CITO	RS .	
	C368,C	411 (47μF/16V)	VCH1166		C257		CCSQCH101J50
						C316,C319	CCSQCH180J50
RESIS	STORS				C253,0		CCSQCH221J50
	R123 (3		ACN7047		C251,0		CCSQCH330J50
		733,R735,R736 (47kΩ)	ACN7077 DCN1092		C255,0	C256,C355,C356	CCSQCH331J50
	R632 (1	609,R613,R624,R627 (10kΩ)			C111		CCSQCH470J50
		631,R633,R638,R654 (10kΩ)				C312,C314,C315	CCSQCH6R0D50
	*	, , , , , , , , , , , , , , , , , , , ,			C317,0		CCSQCH6R0D50
		658,R664,R706 (10kΩ)	DCN1094		C113,0		CEAL101M6R3
		718 (10kΩ)	DCN1094		C502,0	C504	CEAL470M16
		663 (22Ω) 715,R881 (0Ω)	DCN1104 DCN1106		C101 (	C103-C105,C211,C608	CEAT101M10
		R2010,R2020,R2030,R2040	RS1/10S0R0J			C907.C909	CEAT101M10
	,	,,,				C121,C122,C231,C232	CEAT101M16
	,	R4010,R4020,R4030,R4040	RS1/10S0R0J			C327,C373	CEAT101M16
		R4060,R407,R685,R722	RS1/10S0R0J		C203		CEAT102M6R3
	R8000,I R202,R		RS1/10S0R0J RS1/10S101J		C607		CEAT1R0M50
	R700	0010	RS1/10S1R2J		C106		CEATTAUMSU CEAT220M35
			- , . <del> </del>			C207,C250,C350	CEAT470M16
	R807		RS1/16S1201F		C252,0	C352	CEAT470M25
	R806	005	RS1/16S1501F		C324-0	C326	CKSQYB104K25
	R363,R		RS1/16S1503F				
	R825-R R805	021	RS1/16S2000F RS1/16S2701F				
	11000		1.0.710027011				

Mark		Description	Part No.	Mark	No.	De	escription	Part No.
	C130-C C902.C		CKSQYF102Z50 CKSQYF103Z50		FLKY	AS	SSY	
	C100,C	107-C110,C112,C114	CKSQYF104Z25					
		117,C120,C123	CKSQYF104Z25	SEIVII	COND	UC	ions	DE5105A
	C190,C	191,C201,C202,C209	CKSQYF104Z25		IC101 IC102			PE5185A S-806D
		218,C230,C233,C290	CKSQYF104Z25					
		323,C328,C501,C503 602,C612,C901	CKSQYF104Z25 CKSQYF104Z25	SWIT	CHES			
		906,C915	CKSQYF104Z25		S131,5	3132	,S190-S195	ASG7013
	C210		CKSQYF105Z16	0454				
				CAPA	CITOR	RS		05.10.150.4.0
RESIS	STORS				C105 C111			CEJQ470M10 CKSQYB102K50
	R250,R3		RN1/10SE1602D RN1/10SE2702D		-	C104	,C106,C107,C142	CKSQYF104Z50
	R259,R		RN1/10SE2702D		,		, ,	
	R930		RS1/10S1002F	RESIS	STORS	3		
	R909		RS1/10S1502F		R130,F	R190	,R200	RS1/10S1001D
	R914,R9	228	RS1/10S2402F		Other F	Resis	stors	RS1/10S□□□J
	R927	<i>3</i> 20	RS1/10S2701F	07115				
	R929		RS1/10S5601F	OTHE				
		317,R321	RS1/10S8200D		CN102		103 FJ CONNECTOR 8P	08P-FJ
	Other R	esistors	RS1/10S□□□J		CN101		FJ CONNECTOR 8P	52492-1520
OTHE	DC				0		FFC BOTTOM CONNECT	
OIIIL	.110	PCB BINDER	DEF1012				REMOTE RECEIVER UNIT	GP1U28X
	JA602	OPTICAL LINK OUT	GP1FA550TZ		V101		FL TUBE	VAW1058
	CN103	FFC CONNECTOR 15P	HLEM15S-1		V 101		SPACER	VEC1599
	JA101	REMOTE CONTROL JACK					FL HOLDER	VNF1096
		PCB BINDER	VEF1040		X101		CERAMIC RESONATOR	VSS1142
	JA201	4P PIN JACK	VKB1128				(5MHz)	
	JA601	1P PIN JACK (BLK, NI)	VKB1159					
	CN902 CN903	7P FFC CONNECTOR 8P FFC CONNECTOR	VKN1238 VKN1239					
	CN301	16P FFC CONNECTOR	VKN1247	JI	<b>KYLB</b>	A	SSY	
	011101		14414055	SWIT	CHES			
	CN104 CN901	26P FFC CONNECTOR 7P FFC CONNECTOR	VKN1257 VKN1267	01111	S333-9	3337		ASG7013
	CN101,		VKN1765		0000	5001		71007010
		B TO B CONNECTOR 30	P	RESIS	STORS	3		
					All Res	sistor	S	RS1/10S□□□J
M.	JAC2	ΔSSV		OTHE	RS			
		1001			CN301		FJ CONNECTOR 8P	08R-FJ
COILS								
	L1,L2	CHIP BEAD	VTL1089					
CWIT	οш			K	PWSE	RΔ	SSV V22	
SWIT	S101		VSH1009					
	5101		VSH1009	SEMI	COND	UC'	TORS	
САРА	CITOR	S			Q250			DTC124EK
		.C3.C7.C9	CEAT471M6R3		D250			SLR-343DC(NPQ)
	C14,C5		CKSQYF104Z25	SWIT	CHES			
				01111	S200,S	3201	\$230	ASG7013
RESIS	STORS				0200,0	J_U I	,020	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
	R13,R1		RS1/10S1100D	RESIS	STORS	3		
	R17,R19 R18,R2		RS1/10S3300D RS1/10S4700D		All Res		S	RS1/10S□□□J
	R18,R2		RS1/10S4700D RS1/10S62R0D					
	Other R		RS1/10S□□□J	OTHE	RS			
					CN201		FJ CONNECTOR 8P	08R-FJ
OTHE	RS							
	CN3	4P MINI DIN SOCKET	AKP7008					
	JA1 JA5	1P PIN JACK 3P PIN JACK	VKB1122 VKB1150					
	CN1	16P FFC CONNECTOR	VKN1247					
		SCREW TERMINAL	VNE1948					

Mark No. Description Part No.

POWER SUPPLY UNIT (VWR1329)

OTHERS

 ∆
 P202
 PROTECTOR (1.6A)
 AEK7012

 ∆
 P203
 PROTECTOR (800mA)
 AEK7063

 ∆
 F101
 FUSE (2A)
 REK1078

# 6. ADJUSTMENT

There is no information to be shown in this chapter.

# POWER SUPPLY UNIT (VWR1332)

## **OTHERS**

Δ	P101	PROTECTOR (800mA)	AEK7063
$\triangle$	P102	PROTECTOR (1.6A)	AEK7066
$\triangle$	F101	FUSE (2.5A)	REK1102

# 7. GENERAL INFORMATION

## 7.1 DIAGNOSIS

# 7.1.1 SELF-DIAGNOSTIC FUNCTION OF PICKUP DEFECTIVE

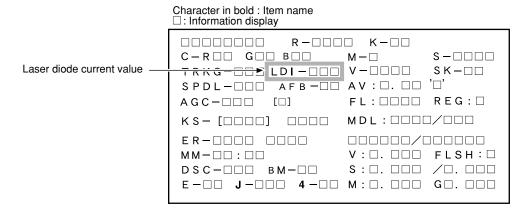
### **Symptom**

- Indicates "No Disc" in FL display.
- Player does not playback, etc..

### **Procedure of Self-Diagnosis**

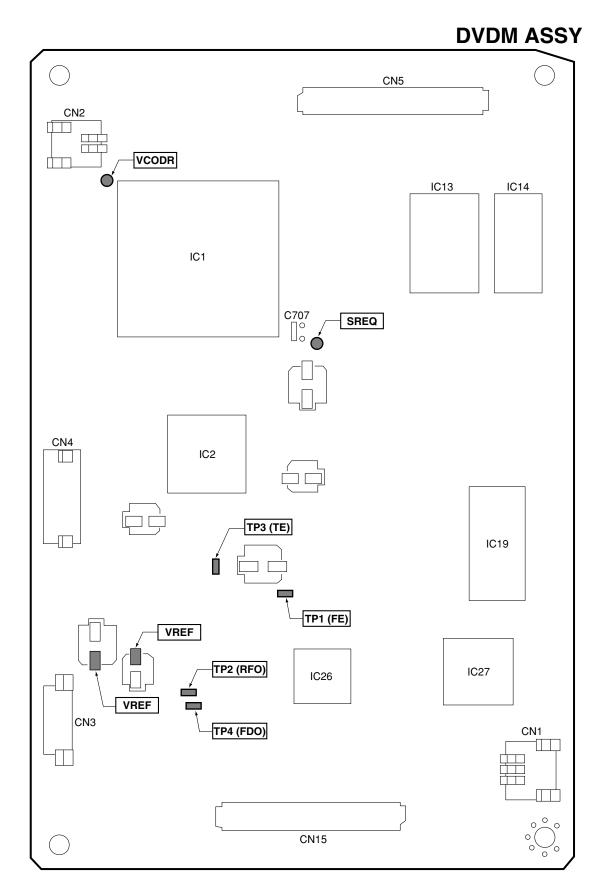
- ① Press the TEST → 1 buttons (of the test mode remote control unit : GGF1067) in the test mode screen, and turn on the laser diode (It light-up for nine seconds.).
- 2 Confirm the indicated value of the laser diode current (LDI).
- ③ When indicated value is more than 110, pickup is defective. → Release the Traverse Mechanism Assy.

**Note:** When a DVD disc is played in the test mode, this function is effective. This function is effective only for DVD pickup (650nm).



Test Mode Screen Display

# 7.1.2 TEST POINTS LOCATION



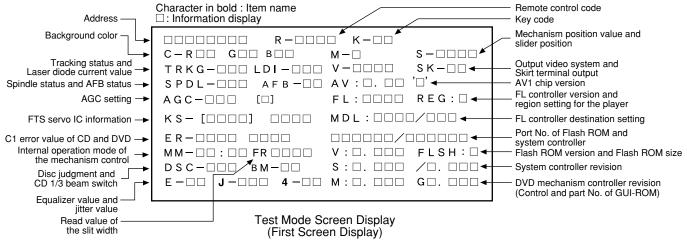
SIDE B

### 7.1.3 TEST MODE SCREEN DISPLAY

### **■ TEST MODE SCREEN DISPLAY**

Consecutive double-OSD display is supported during test mode. The screen is composed 10 lines with a maximum of 32 characters per line. It can't be used with the debugging display mode together.

### Screen Composition



### Caution:

The first screen and second screen switch by pressing [DISPLAY] key of the remote control unit.

It is only a version display part on the lower right of the screen those contents of display change.

ATB: ON/OFF information display and AGC manual setting display deleted with the second generation.

The displays of Tilt error value, Tilt servo status and pickup DVD/CLD display deleted with the third generation becomes LD part is deleted.

### Description of Each Item on the Display

### (1) Address indication

The address being traced is displayed in number.

DVD: ID indication (hexadecimal number, 8 digits)

[ \* \* \* \* \* \* \* \* ]
CD : A-TIME (min. sec.) [ 0 0 0 0 \* \* \* \* ]
(Note : For DVDs, decimal-number indication is possible.)

### (2) Code indication of the remote control unit [R - \* \* \* \*]

The code for the key pressed on the remote control unit, which is received by the FL controller, is displayed while the key is pressed. In the case of the double code, the second code will be displayed.

### (3) Key code indication for the main unit [K - \* \* ]

The code for the key pressed on the main unit, which is received by the system controller, is displayed while the key is pressed.

#### (4) Background color indication [C - R\* \* G\* \* B\* \*]

### (5) ① Tracking status [TRKG - \*\*\*]

Tracking on [ON]
Tracking off [OFF]

2 Laser diode current value [LDI - \*\*\*]

#### (6) (1) Spindle status [SPDL - \* \* \*]

Spindle accelerator and brake, free-running	[A/B]
FG servo	[FG]
Rough, velocity phase servo	[SRV]
Offset addition, rough, velocity phase servo	[O_S]
② AFB status [AFB - * *]	
ON	[ON]
OFF	[OFF]

### (7) Mechanism position value [M - \*]

Position code [1] to [3]

#### (8) Slider position [S - \* \* \* \*]

CD TOC area	[IN	]
CD active area	[CD	]

### (9) AGC setting [AGC - \* \*]

AGC on	[AGC-ON]
AGC off	[AGC-OFF]

# (10) Output video system [V - \* \* \* \*] NTSC system [NTSC] PAL system [PAL ] Auto-setting [AUTO] Skirt terminal output [SK - \* \*] VIDEO [00] S-VIDEO [01] RGB [02]

Note: Display only the model which can do the output setting of skirt terminal.

#### (11) FTS servo IC information

DSP coefficient indication [KS - [\*\*\*\*] \*\*\*\*] Displays the address (four digits) of the specified coefficient

Displays the address (four digits) of the specified coefficient and the setting value (four digits) with [TEST] and [9] keys.

#### (12) Error rate indication

① C1 error value of CD [ER - C1 \* \* \* \* ]
② C1 error value of DVD [ER - \* \* \* \* \* \* \* \* ]

# (13) Internal operation mode of mechanism controller [MM - \* \* : \* \*]

Internal mechanism mode (2 digits) and internal mechanism step (2 digits) of the mechanism controller

### (14) ① Disk sensing [DSC - \* \* \*]

The type of discs loaded is displayed. [DVD], [CD], [VCD], [ ]

② CD 1/3 beam switch [BM - \* \*]

### (15) ① Equalizer value [E - \* \*]

② Jitter value [J - \* \*]

nake the jitter four times, and renew it in every one second. [4-\*\*]

CD is effective only in the jitter value.

### (16) Version of the AV-1 chip [ AV : \* . \* \*' \*' ]

### (17) ① Version of the FL controller [FL: \* \* \* \*]

② Region setting of the player [REG:\*]
Setting value [1] to [6]

# (18) Destination setting of the FL controller [MDL: \*\*\*\*/\*\*\*]

For charactors in front represent the type of model : There charactors that follow represent the destination code. J:/J, K:/KU, /KC, /KU/KC, R:/RAM, /RL, /RD, /LB, WY:/WY

# (19) The part number of the flash ROM and system controller [\* \* \* \* \* \* / \* \* \* \* \* \* \*]

① Part number of the flash ROM
(Example) VYW1536-A = W1536A
(Example) PD6256A9 = 6256A9

② Part number of the system controller <Rear>
(Example) PD3381T1 = 3381T1

- (20) ① Version of the flash ROM [V:\*.\*\*]
  - 2 Flash ROM size [FLSH = \*]

### (21) Revision of the system controller [S:\*.\*\*/\*.\*\*]

- ① Revision number of the external ROM part (flash ROM) of the system controller < Front>
- ② Revision of the internal ROM part of the system controller <Rear>

# (22) Revision of the DVD mechanism controller

[M:\*.\*\*\*]

Revision number of the external ROM part (flash ROM) of the DVD mechanism controller

# (23) Control and part numbers of the GUI-ROM [GUI: \* \* \* \*]

No GUI model displays as "—— / ———".

OEM model displays the part number of GUI-ROM
[GUI: \* \* \* \* \*]

# (24) Read value of the slit width (for Stop position detection)

[FR \* \* \* \*]

Read value of Reverse direction

Read value of Forward direction

### ■ DEBUGGING SCREEN SPECIFICATION FOR THE MECHANISM CONTROLLER

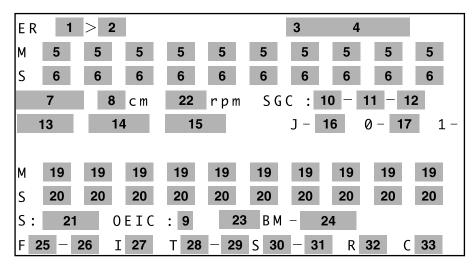
• This specifications is subject to change without notice.

### 1 Indication Method of The Mechanism Controller Debugging Screen

A debugging screen of the mechanism controller is indicated when pressing the test mode remote control unit [GGF1067] in order of the [ESC] and [CHP/TM] buttons.

Releace from debugging screen display of the mechanism controller with the ESC button.

### 2 Screen Layout



### **3 Indication Contents**

 The error that became the trigger that an error of 2 occurred.

There are many cases same as 2.

2. The error number that transferred to the system controller

Refer to the error list about contents of error number.

- 3. Code read in state (it does not support in this unit)
  When X is indicated, ID or subcode are not able to read in.
  When X is not indicated, they are able to read in.
- ID or subcode (it does not support in this unit)
   Subcode indicates the A time.
- Inside mode of the mechanism controller when an error of 1 occurred

It can indicate to a maximum 10 mode. Indicate it in order of an old mode from the left, and go right, and become a new mode. Indicate only a nest share of the mode.

6. Processing step of inside mode of 5

It can grasp the mode reaching an error and transition of step by watching 5 and 6 and it can specify the occurrence place of most errors.

7. Disk information in the mechanism controller

? : Indistinctness NO : There is no disc DVD 1 : DVD single layer DVD 2 : DVD dual layer

CD : CD

CDR : CD-R or CD-RW CDR P : PRD of CD-R or CD-RW 8. As a result of 8cm /12cm distinction

? : Indistinctness (undistinction)

8:8 cm 12:12 cm

9. OEIC gain (it does not support in this unit)

H : OEIC HIGH gain L : OEIC LOW gain

#### 10. SGC gain for LD of 780nm

It indicates a step using in the mechanism controller inside with a hexadecimal number.

Set the gain so that S curve becomes 1.8V (p-p) in disc distinction.

### 11. SGC gain for LD of 650nm For L0.

It indicates a step using in the mechanism controller inside with a hexadecimal number. Set a gain so that S curve becomes 1.8V (p-p) in disc distinction.

### 12. SGC gain for LD of 650nm For L1.

It indicates a step using in the mechanism controller inside with a hexadecimal number. Set a gain so that a S curve becomes 1.8V (p-p) in disc distinction.

#### 13. RF count value for disc distinction

RF count value to use the disc distinction. It compares threshold value of 14 and 15 and distinguishes the disc.

14. Disc distinction threshold value (DVD and CD) Threshold value of the disc distinction. Distinguish it from DVD if bigger than this value, and distinguish it from CD if small. Disc distinction threshold value (CD and unrecorded disc)

Threshold value of the disc distinction. Distinguish it from CD if bigger than this value, and distinguish it from an unrecorded disc if small.

#### 16. Current jitter value

Indicate the value that was read in from the MY-CHIP in DVD, and indicate the value that was read in from the servo DSP in CD.

- 17. Focus balance setting value of L0
- 18. Focus balance setting value of L1
- Current mechanism controller inside mode

   (it does not support in this unit)

   It can indicate to a maximum 10 modes. Indicate only a nest share of the mode.

### 20. Processing step of 11 inside modes

(it does not support in this unit)

It can grasp the current mode, the mode reaching it and transition of step by watching 19 and 20.

### 21. Spindle control state of MY-CHIP

(it does not support in this unit)

OFF: Motor off condition
A/B: Accelerator and brakes

FG: FG servo

RVP : Rough speed phase servo

ORVP: Rough speed phase servo of offset addition

#### 22. Rotation number of spindle motor

Do not FG read in? indication (during spindle stop).

#### 23. Tracking error generation system

(it does not support in this unit)

1: 1 beam (DPD)

3: 3 beams

24. TZC count value (it does not support in this unit)

The value that counted the number of TZC for one rotation in the tracking open state.

When this value is more than 512 with CD, set it in 1 beam because the eccentric is large.

DVD does not measure it because it is 1 beam fixed (indication is 0000).

- 25. It indicates the frequency that entered the focus backup Hexadecimal number indication. Counter does not reset till turns the power off after turning it on. Due to a 1 byte counter, next of FF becomes 00.
- 26. It indicates focus backup limit frequency with the hexadecimal number

Initial value is 14H, it does decrement whenever enter the focus backup and it gives up backup if it became 0. Then the error is generated. After reverted from the backup, When not enter the backup and pass fixed time (1500ms), return to initial value again.

27. It indicates the frequency that entered the internal circumference plunging into backup of the sled Hexadecimal number indication. Counter does not reset till turns the power off after turning it on. Due to a 1 byte counter, next of FF becomes 00.

#### It indicates the frequency that entered the tracking overrun backup

Hexadecimal number indication. Counter does not reset till turns the power off after turning it on. Due to a 1 byte counter, next of FF becomes 00.

29. It indicates the limit frequency of tracking overrun backup with a hexadecimal number
Initial value is 03H, it does decrement whenever enter the tracking overrun backup and it gives up backup if it became

# 30. It indicates the frequency that entered sled overrun backup

Hexadecimal number indication. Counter does not reset till turns the power off after turning it on. Due to a 1 byte counter, next of FF becomes 00.

31. It indicates the limit frequency of sled overrun backup with a hexadecimal number

Initial value is 03H, it does decrement whenever enter the sled overrun backup and it gives up backup if it became 0.

32. It indicates the frequency that entered the tracking close NG backup

Hexadecimal number indication. Counter does not reset till turns the power off after turning it on. Next of FF is be a 1 byte counter in 00.

The hexadecimal number indication which indicates the frequency that reads

#### 33. ID/subQ, and entered NG backup

Hexadecimal number indication. A counter does not reset it till cuts it off after turning it on. Due to a 1 byte counter, next of FF becomes 00.

- 34. An address to indicate in 35 Set it by using RS232.I (an address) Set it with DA.
- 35. Contents of an address indicated in 34.

# 7.1.4 ERROR CODE

## Error codes that are displayed on the FL display without using the remote control unit

FL Display	Possible causes	Operation of the unit
AV1 VER	AV-1 chip is not a match with the program of system controller	The sound may not out with the specific audio.
CPU AERR	CPU address error (Hardware is unusual.)	No operation
DMA AERR	DMA address error (Hardware is unusual.)	No operation
FLASH ID	Difference in versions of the internal ROM of the system controller and of the flash ROM, or bus line failure or reverse installation	No operation
FLASH WRP	Write protect error of the flash ROM	No operation
FLASH SIG	Difference in part number of the flash ROM (When the ROM which could't be used was used.)	No operation
FLASH SUM	Check sum error of the flash ROM (It exceeds the regular size.) or reverse installation (Hardware is unusual.)	No operation
FLASH SIZE	Size error of the flash ROM (Use 4 or 8 M-bit.)	No operation
ILLGAL	The system controller fetched a code other than an operation code (Hardware is unusual.)	No operation
RESERVE	Undefined interrupt (Hardware is unusual.)	No operation
SLOT	Inappropriate slot command issued (Hardware is unusual.)	No operation

Error codes that are displayed on the FL display by using the remote control unit (Mechanism controller error)

To display: ESC + DISPLAY + DISPLAY; Location of the display: At the two digits of center of the FL display

To display the error history: ESC + DISPLAY + One shot; Location of the display: TV screen

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
11	Search timeout	Search could not be complete within 7 seconds.	Search could not be complete within 7 seconds, and it could not enter the target area within 7 seconds by VCD scan.	CD : Stops, DVD : Continues operation
12	Search retry error	A search could not be completed after 3 retries, search backup was executed 4 times, or in a case of timeout (6 seconds) while the unit was tracing 11 tracks or more beyond the target while the search operation was converging.	Backup against slider skip was executed 4 times during a search, or slider skip twice resulted in starting from the read-in point.	CD : Stops, DVD : Continues operation
19	Tracing timeout while converging	Timeout (10.5 seconds) while tracing at the stage of convergence of a search.		Stop
1B	Index 0 search error		During Track (Index) Search, the search for the beginning of a program could not be completed within 3 seconds (20 seconds in the case of Index Search) after positioning based on the TOC data was completed.	Stop
22	Timeout of slider inner circumference	Inside switch could not ON within 3 second	nside switch could not ON within 3 seconds.	
23	Timeout of slider outer circumference	Inside switch could not OFF within 2 secon	nds.	Stop
	No FOK pulse during playback CLVA	When the focus was deviated continuously 20 times.		Adjusts focus at the innermost circumference and tries to return to its position where the error was generated (for 3 times),then opens. If the same error persists after one retry, the tray opens. (No FOK pulse)
38	Disc-type- sensing error	If normal starting was impossible in the foll be retried if other errors occure excepting ("33" was occured continuously 3 times, it (1) startup with the first disc-type-sensing r by designating the disc type, (3) forced stathe disc type.	Open	

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the
39	SGC converge timeout	SGC could not converge during detects the peak		Open
41	Spindle timeout	The unit did not enter Stop mode within 10 seconds of is	ssuance of a Stop command.	Stop
48	Spindle FG transition timeout	The spindle could not converge into within $\pm$ 12% of the target FG rotation speed within 10 seconds after spindle kick. The first time after startup (the first time after disc distinction), it doesn't become the number of the target rotation within five seconds. The first time after startup, detects the abnormal rotation number of high-speed continuously 3 loops. DVD: 5 to 9 mS , CD: 40 to 60 mS		Stops. (FG timeout)
49	Spindle PLL transition timeout	within five seconds. Detects the abnormal high-speed or low-speed rotations. DVD: 5 to		Stops. ("73" is displayed during starting process.)
4A	Spindle lock timeout	Spindle could not lock more than 1.5 seconds before sta	Stops. ("73" is displayed during starting process.)	
51	Auto sequence timeout of peak	ABUSY did not return within 1 second after the DDTCT (peak detection) command was sent.		Stop
52	Auto sequence timeout of focus jump down	ABUSY did not return within 30 mS after the FJMPD (Focus jump 1 to 0) command was sent.		Stop
53	Auto sequence timeout of focus	ABUSY did not return within 30 mS after the FJMPU (Focus jump 0 to 1) command was sent.		Stop
54	Auto sequence timeout of play AGC	ABUSY did not return within 50 mS after the GSUMON (play-AGC-measuring) command was sent.		Stop
55	Auto sequence timeout of disc-type- sensing	ABUSY did not return within 2 seconds after the DJSRT (disc-sensing) command was sent.		Stop
56	Auto sequence timeout of ATB2	ABUSY did not return within 1 second after the TBLOFS (Internal ATB after the completion of external ATB) command was sent.		Stop
57	Auto sequence timeout of tracking servo ON	ABUSY did not return within 500 mS after the TSON (tracking servo ON) command was sent.		Stop
58	Auto sequence timeout of ATB1	ABUSY did not return within 200 mS after the TBL (external ATB) command was sent.		Stop
59	Auto sequence timeout of focus gain adjustment	ABUSY did not return within 2 seconds after the FGN (focus gain adjustment) command was sent.		Stop
5A	Auto sequence timeout of tracking gain adjustment	ABUSY did not return within 2 seconds after TGN (tracking gain adjustment) command was sent.		Stop
5B	Auto sequence timeout of offset adjustment	ABUSY did not return within 1 second after the CMDAVE (offset adjustment) command was sent.		Stop
5C	Auto sequence timeout of modulation factor measurement	ABUSY did not return within 200 mS after the ADJMIR (modulation factor measurement) command was sent.		Stop
5D	Auto sequence timeout of auto focus bias	ABUSY did not return within 2 seconds after the AFB (auto focus bias) command was sent.		Stop
5F	Auto sequence already busy	A command could not be sent because ABUSY was low. ABUSY did not return within 200 mS after TLV command was sent.		Stop
62	Pause retry error	Pause mode could not be restored within three retries after it had been released.		Continues operation

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
71	ID can not read during tracing	An ID could not be read for 1 second or more.	or 1 second or more.	
72	Subcode check failure during playback	No frame could be read for 3 seconds or more.		Stop
73	ID can not read at the startup	An ID could not be read within 1 second after the AFB adjustment had been finished.		Opens (ID readout failure)
74	Subcode check failure during startup		No subcode could be read within 3 seconds after AFB adjustment had been finished.	
81	Timeout for reading TOC of the mechanism controller		TOC readout took 30 seconds or more.	Stop
82	Timeout for reading TOC of the system controller		Reading TOC of the system controller took 30 seconds or more.	Stop
A1	Communication timeout of DSP command	A command could not be issued to DSP because Command Busy (XCBUSY) was in force (XCBUSY = L) for a specified time (about 200 mS).		No operation
A2	Communication timeout for reading DSP coefficient	Command Busy (XCBUSY) was in force for a specified time (about 200 mS) before and after a coefficient read command was issued to DSP, or the address echo-back after command issuance did not match the setup address.		No operation
АЗ	Communication timeout for writing DSP coefficient	Command Busy (XCBUSY) was in force for a specified time (about 1024 mS) before and after the coefficient write command was issued to DSP.		No operation
A4	Communication timeout for continuously writing DSP coefficient	Command Busy (XCBUSY) was in force for 200 mS during continuous coefficient writing, or before and after a continuous write command was issued to DSP.		No operation
B1	Timeout error for backup	In the tracing state during the backup sequence, second or more. In the backup sequence, trackin could not be completed even if more than 500 mS was issued.	Stops	
B2	Retry error for backup	Tracing impossible after retring the tracking ON for 3 times in the backup sequence.		Stops
В3	Retry error for trace	During tracing, runaway was detected after three iterations of backup operations for detecting runaway.		Stops
С3	Detection of tracking overcurrent	During playback, the overcurrent detection port was continuously.	Stops (the mechanical controller operates independently).	
(C5)	Short-circuit test corresponding error	While the power was on, the overcurrent detection port was at L for 40 ms or more continuously.		Turns off the power instantly (No indication on the FL display and no writing to flash memory)
E3	Violation against digital copy guard			Stops
F5	Tray being pushed	The tray switch that had been Open mode was forcibly changed to a mode other than Open by an external force.		Closes
F8	Loading timeout	Loading, unloading or clamping could not be comp 5 seconds).	Reverses the loading direction. It timeout is repeated upon retry, the unit stops.	
FC	Focus	The following error occured eight times. (1) Focus completed even if more than two seconds after servo DSP) was sent. (2) Focus IN sequence was completed.	Stops wherever possible then opens (stops in the case of side B).	

Error codes that are displayed on the FL display by using the remote control unit (Device error)

To display: ESC + DISPLAY + DISPLAY; Location of the display: At the two digits of left of the FL display

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit	
bit3=1 08 etc.	AV1 access error (read, write NG)			No operation or it becomes debugging indication if the power is able to ON.	
bit2=1 04 etc.	MY CHIP access error				
bit1=1 01 etc.	SRAM access error				

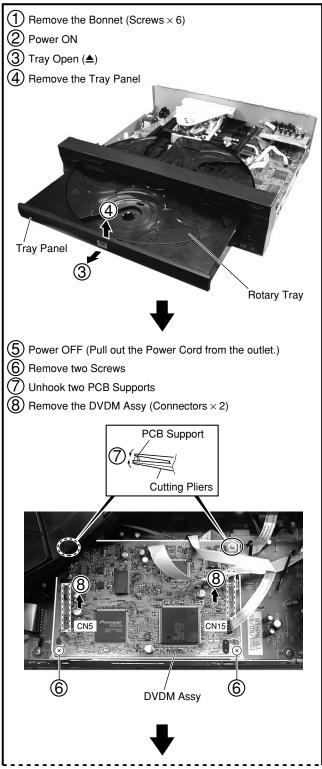
### 7.1.5 DISASSEMBLY

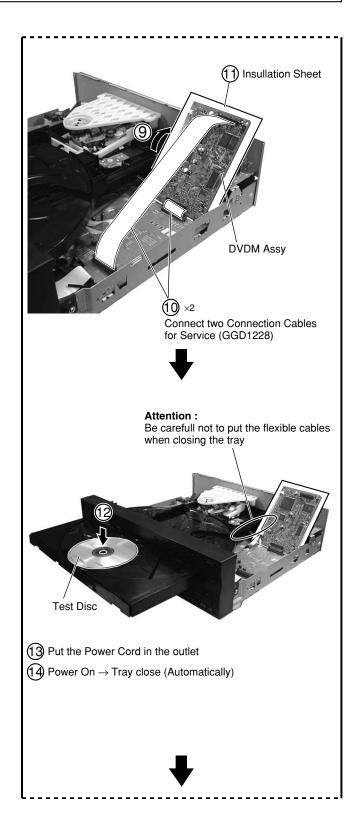
# **DIAGNOSIS OF PCBs**

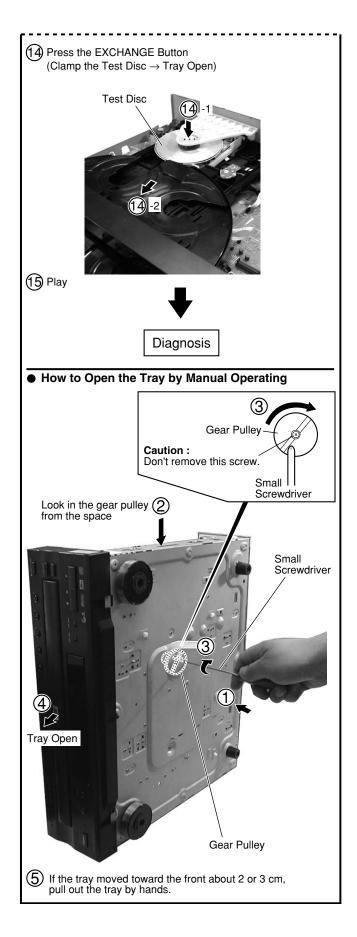
### Note

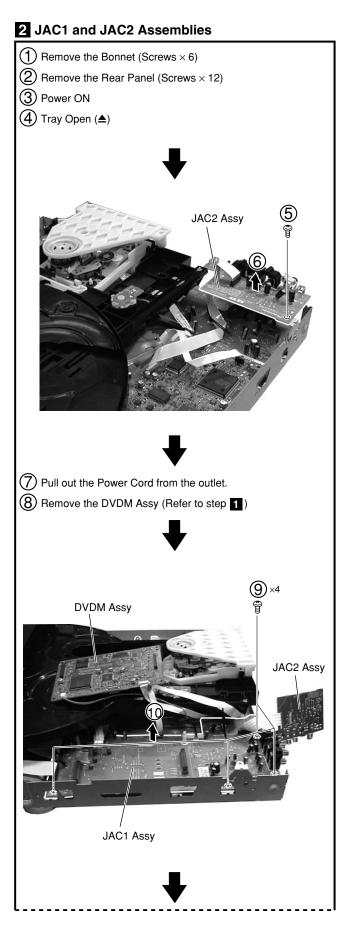
When diagnosing the unit, be sure to use two connection cables for service. (Part No: GGD1228)

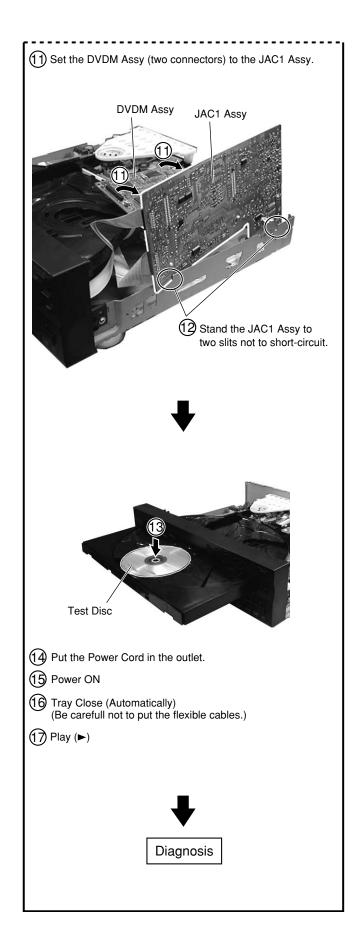
# 1 DVDM Assy

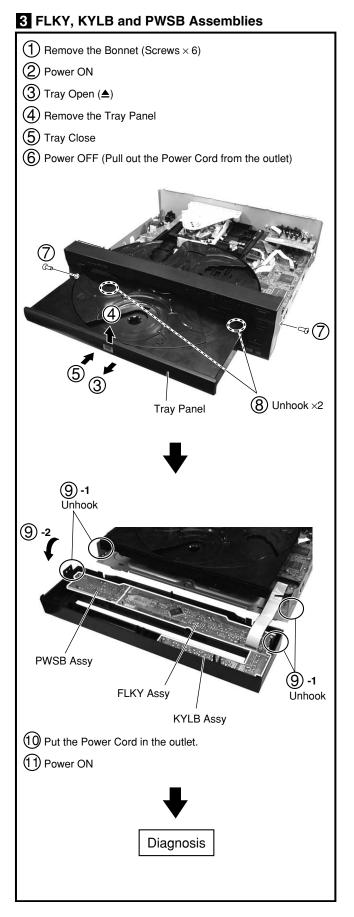






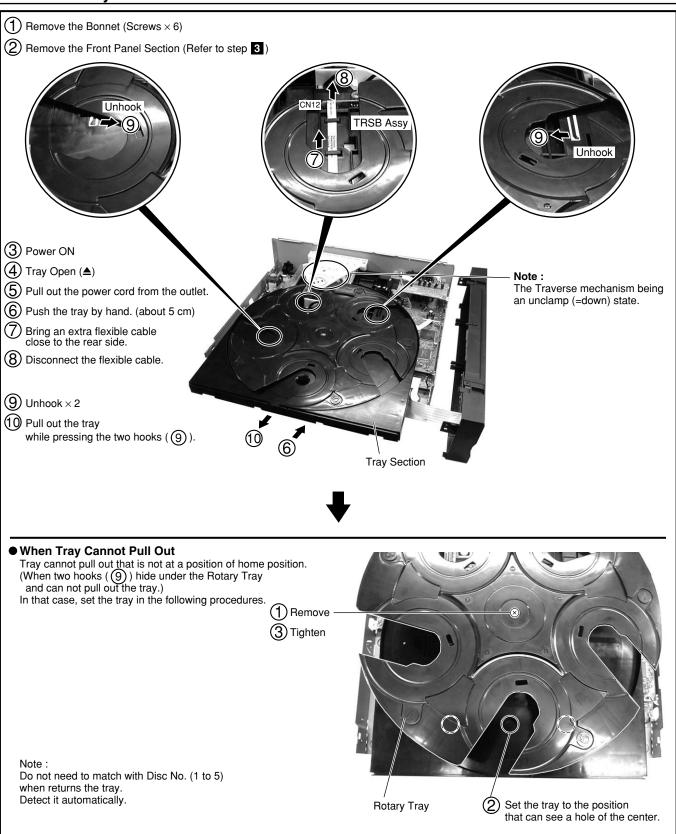


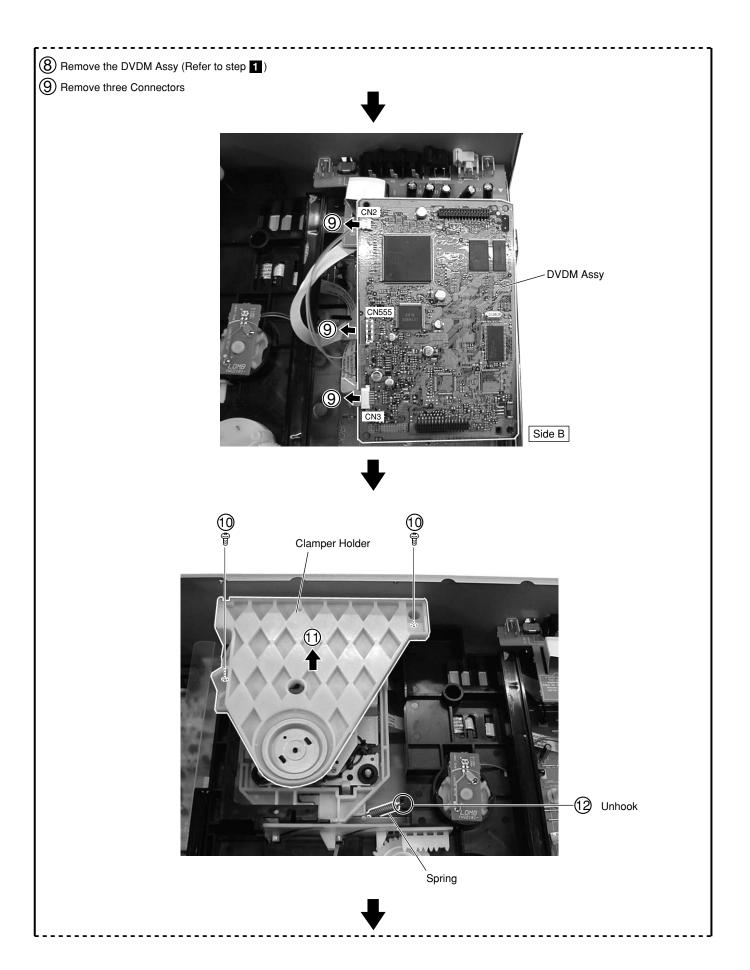


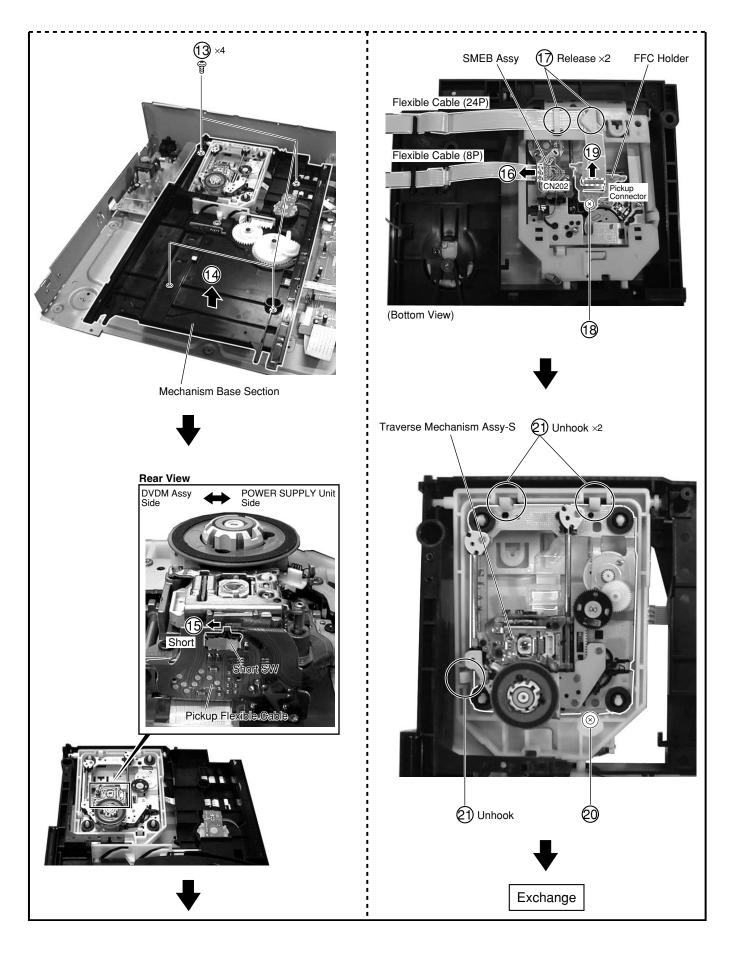


# TRAVERSE MECHANISM ASSY-S

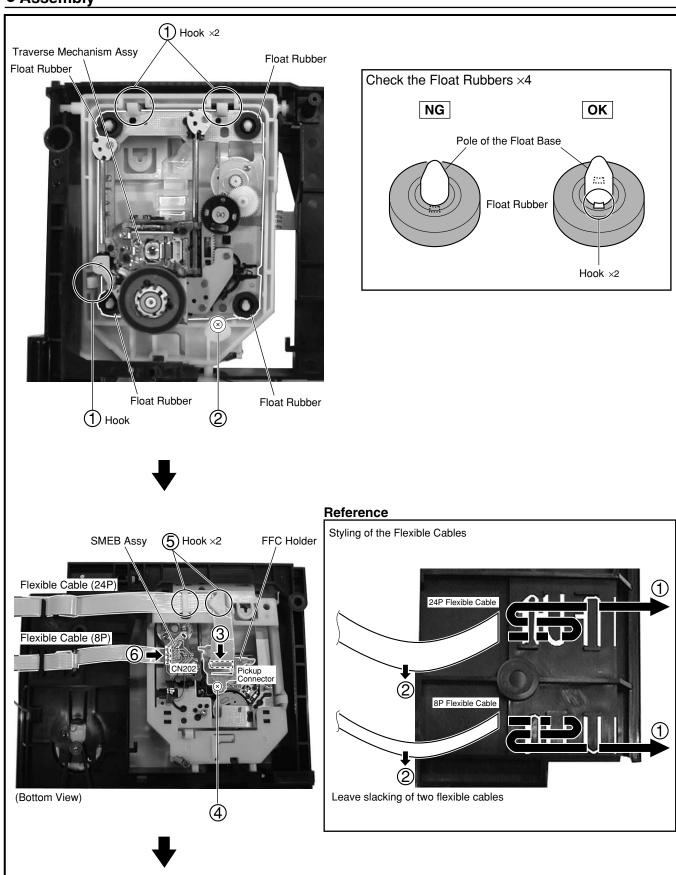
# Disassembly

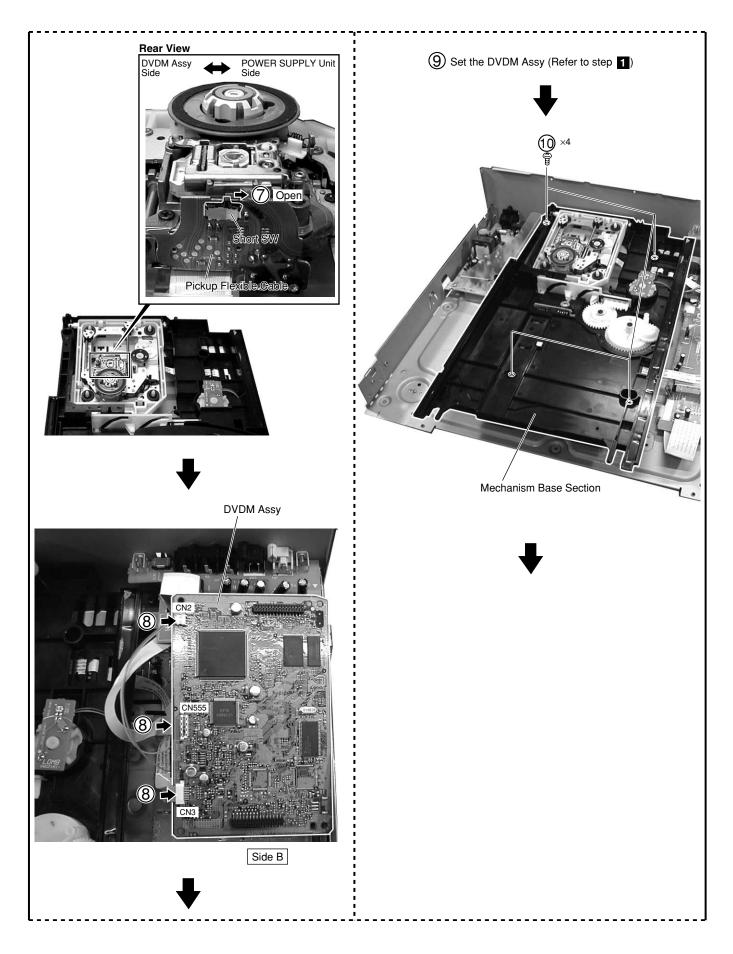


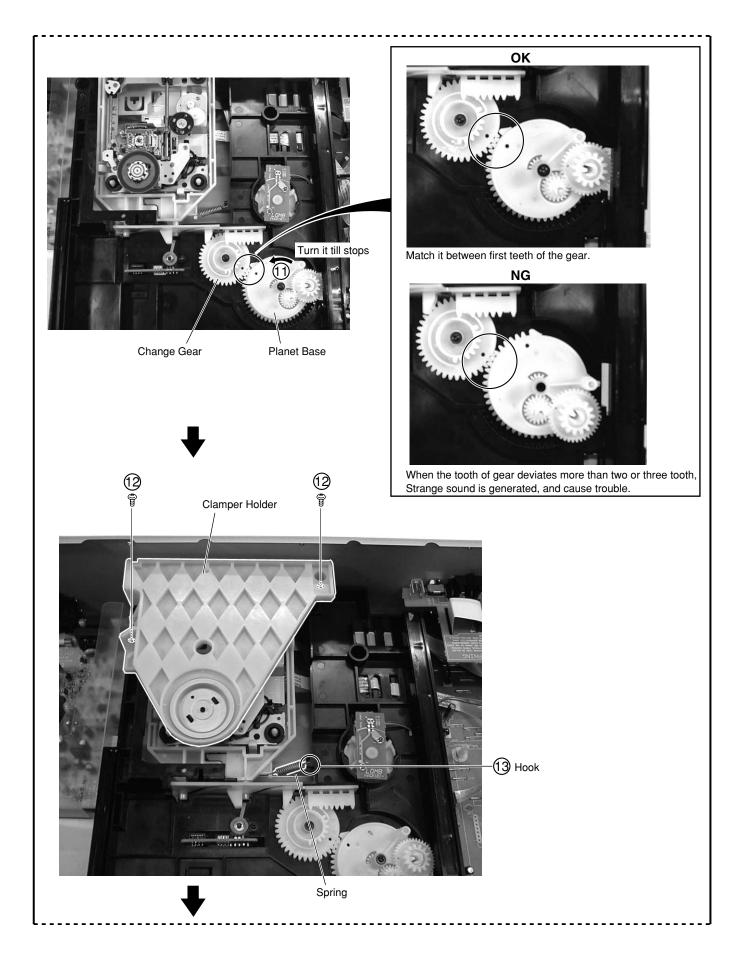


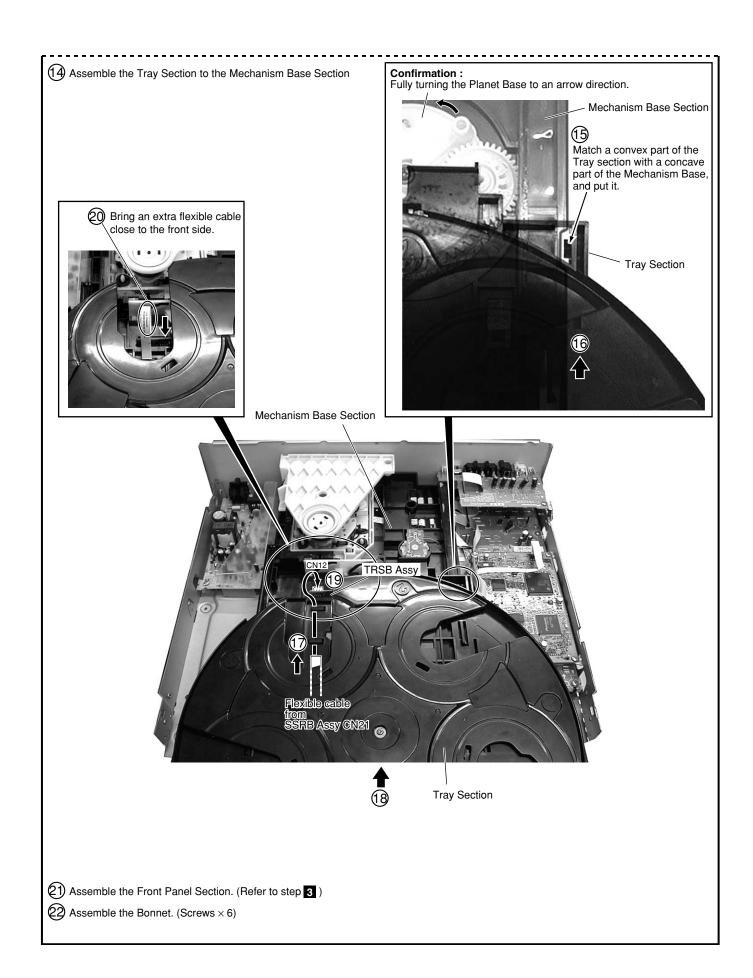


# Assembly

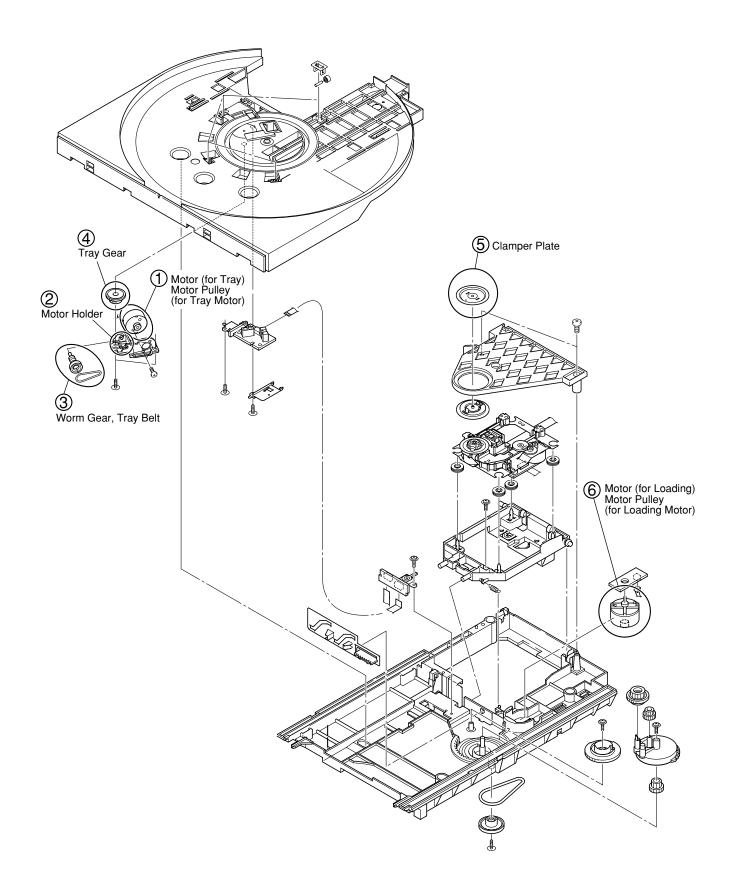






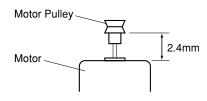


# • Important Points when Assembling the Mechanism Section

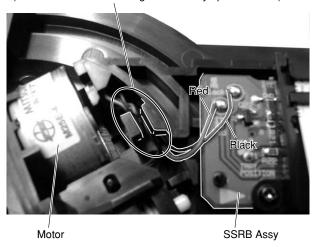


# 1 Motor (for Tray), Motor Pulley (for Tray Motor)

Adjust the height of the Motor Pulley to 2.4mm.



Wiring lead wires into the hook of Motor Holder. (So that lead wires are caught at the Tray open or close.)



## 2 Motor Holder

Apply Lubricating Oil (GYA1001) to the holder part.



Apply Lubricating Oil (GYA1001)

## (3) Worm Gear and Tray Belt

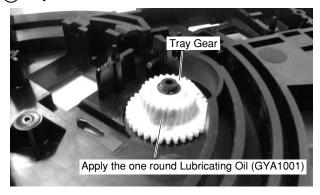
Tray Belt

Be carefull not to damage it in exchange.



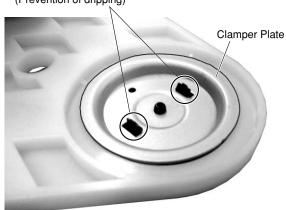
Worm Gear Apply the one round Lubricating Oil (GYA1001)

# (4) Tray Gear

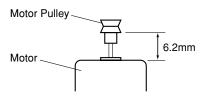


## (5) Clamper Plate

Apply the adhesive on the inside of hooks. (Prevention of dripping)

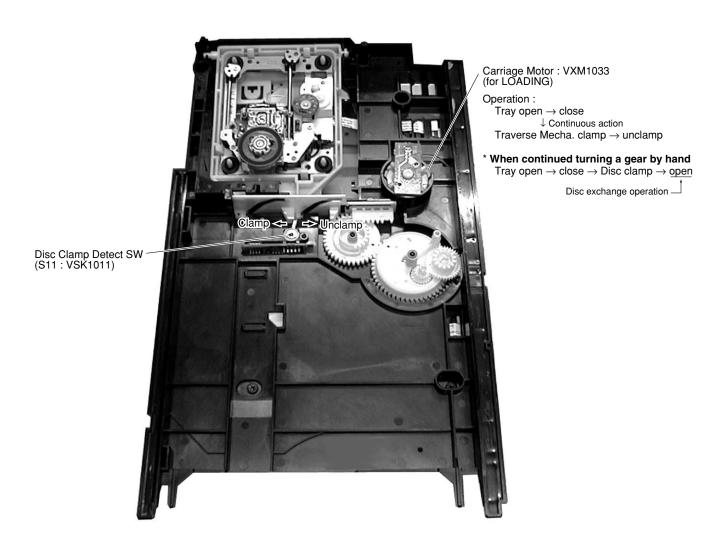


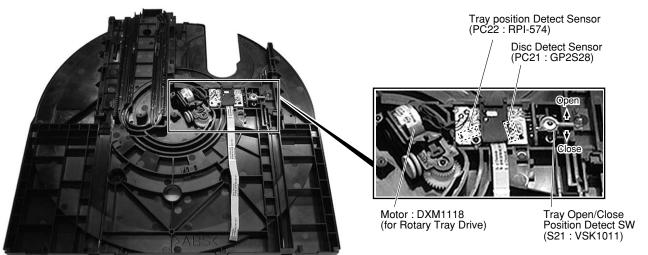
# 6 Motor (for Loading), Motor Pulley (for Loading Motor) Adjust the height of Motor Pulley to 6.2mm.



## 7.1.6 TROUBLE SHOOTING FOR MECHANISM SECTION

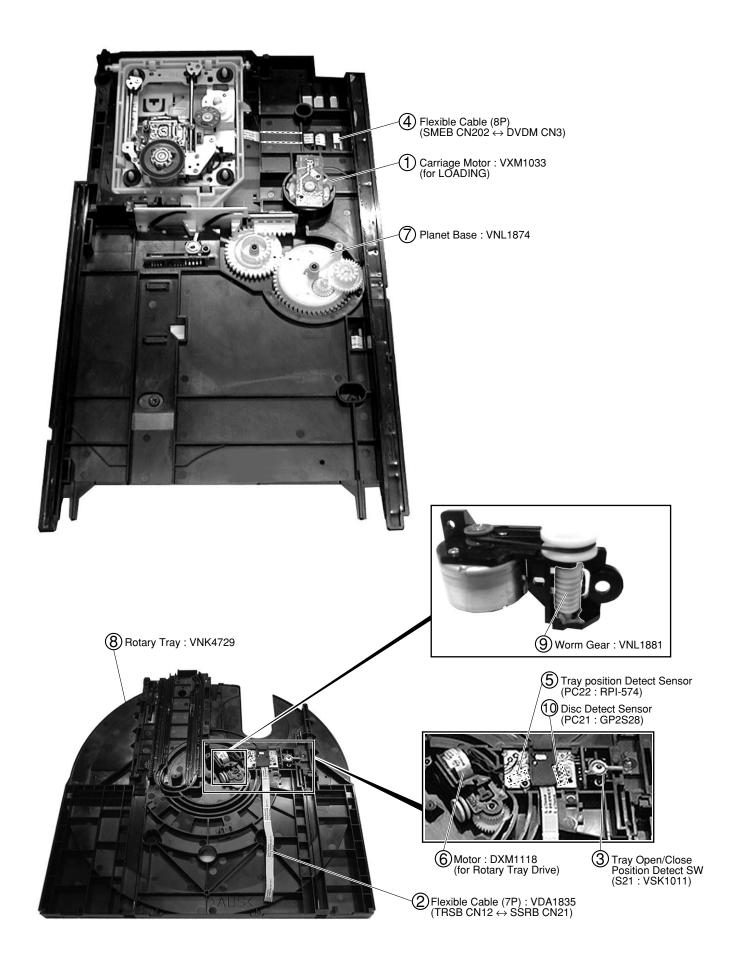
**Location of Motors and Switches** 





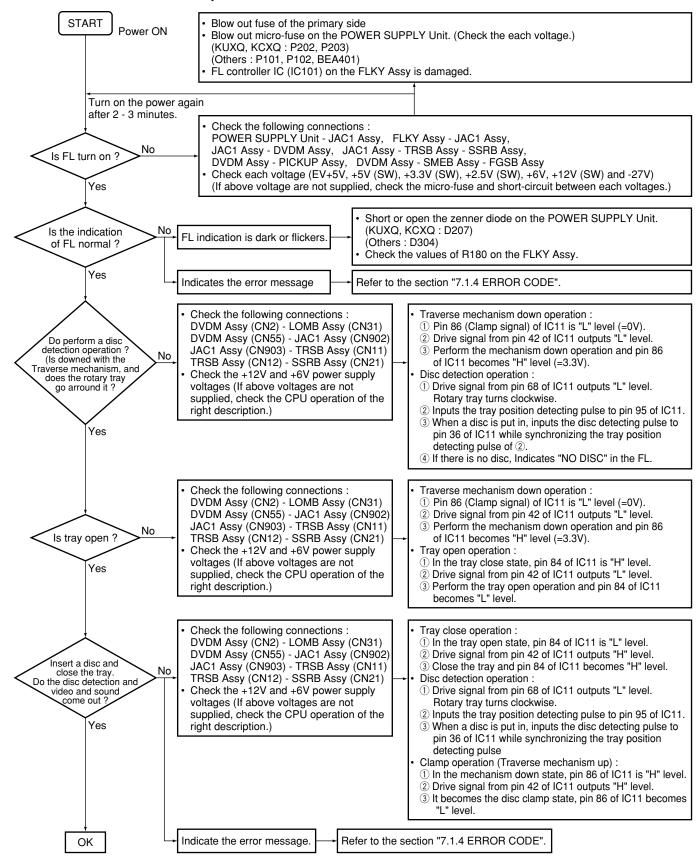
# **Trouble Shooting**

Condition	Condition	Trouble Point
Tray does not open. (Nothing works)  POWER OFF Key → "OFF" indication does not disappear  (OPEN) key → "OPEN" indication does not disappear  (PLAY) key → "OPEN" indication of "OPEN" from "NO DISC", and stop by the condition.  (When a disc is not put in the Tray.)	Loading Motor is damage, disconnection or contact defectiveness.	••
Tray does not open. (Hear the clatter continuation sound.) It becomes the state that flashed indication of " 1 2 3 4 5 ".	Contact defectiveness of Flexible Cable (7P) or the connector comes off, etc  Tray Open/Close Position SW (S21) is damage or contact defectiveness, etc	<b>▲</b> ② <b>▲</b> ③
Tray opens it in POWER ON freely, and stay by the state.	Contact defectiveness of Flexible Cable (8P) or the connector comes off, etc	<b>4</b>
Rotary Tray does not stop at the home position. (May stop on the way of rotation suddenly.)	Tray Position Detect Sensor (PC22) is damage or contact defectiveness, etc	▲⑤
"CHANGE" indication does not change in POWER ON, and Rotary Tray does not move. (In this case only opening and closing of Tray are possible.)	Motor for Rotary Tray Drive is damage, disconnection or contact defectiveness.	<b>▲</b> ⑥
Hear the clatter sound in clamp of the disc.	A gear of Planet Base deviates.	▲⑦
Rotation of Rotary Tray does not stop in POWER ON. In this case indication more than two which seems to be " 1 2 3 4 5 " lights, and abnormal indication appears.	Slit part of bottom of the Rotary Tray is damaged.	▲8
When Rotary Tray turns, hear the rattle and large sound.	Damage of Worm Gear or grease is insufficient.	<b>▲</b> ⑨
Playback even if indicates it with "NO DISC".  Take time till playbacks.	Disc Detect Sensor (PC21) is damage or contact defectiveness, etc	<b>▲</b> ①



### 7.1.7 TROUBLE SHOOTING FOR ELECTRICAL SECTION

- No Power ON
- FL is not turned ON
- FL indication is abnormal
   No Play



## 7.2 PARTS

## 7.2.1 IC

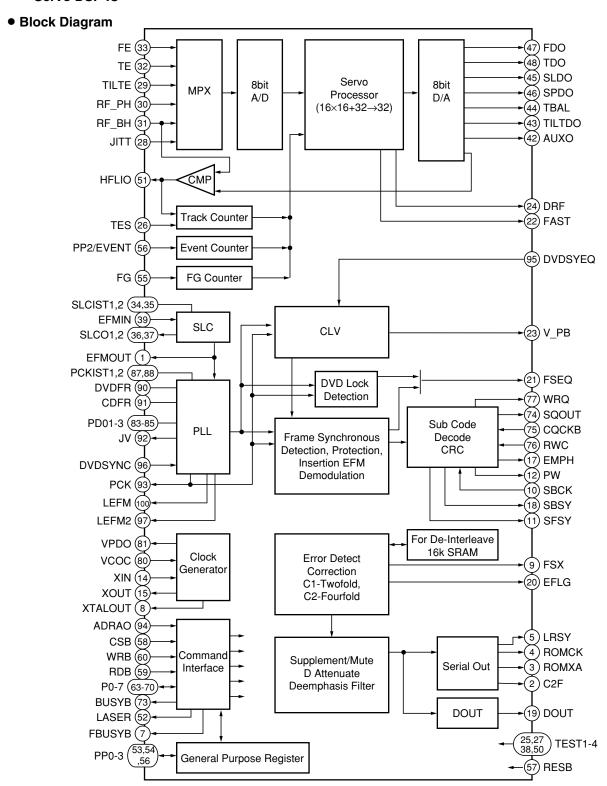
• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

#### List of IC

LC78652W, PD3410A, MB86373B, PE5185A

# **■ LC78652W (DVDM ASSY : IC2)**

Servo DSP IC



# ●Pin Function

EPMOUT	No.	Pin Name	I/O	Function
Corr	_			
3   ROMAX				· ·
A ROMCK				- '
S				·
6 PR3	5			
7         FBUSYB         O         Busy signal output of DSP process operation         N ch-OD output           8         XTALOUT         O         CD 1 frame sync. signal output           9         FSX         O         CD 1 frame sync. signal output           10         SBCK         I         Subcode reading out clock input           11         SFSY         O         Frame sync. signal output of subcode           12         PW         O         Subcode P, Q, R, S, T, U, V and W output           13         VSS         — GND pin           14         XIN         I         Connect a crystal resonator (16.9344MHz)           15         XOUT         O         Connect a crystal resonator (16.9344MHz)           16         DVDD1         — 3.3V power supply of the oscillation circuit           17         EMPH         O         Monitor pin of the deemphasis           18         SBSY         O         Sync. signal output of the subcode block           19         DOUT         O         Audic EAL data output           20         EFLG         O         Detection monitor of the CDDVDV frame sync. signal           21         FSEQ         O         Detection monitor of the DDVDV frame sync. signal           22         FAST </td <td></td> <td></td> <td></td> <td></td>				
8         YTALOUT         O         Esternal system clock output           9         FSX         O         CD 1 frame sync. signal output           10         SBCK         I         Subcode reading out clock input           11         SFSY         O         Frame sync. signal output of subcode           12         PW         O         Subcode P, Q. R, S. T, U, V and W output           13         VSS         – GND pin           14         XiN         I         Connect a crystal resonator (16.9344MHz)           15         XOUT         O         Connect a crystal resonator           16         DVDD1         -         3.3V power supply of the oscillation circuit           17         EMPH         O         Monitor pin of the deemphasis           18         SBSY         O Sync signal output of the subcode block           19         DOUT         O         Audio EIAJ data output           20         EFLG         O         Error correction state monitor of the error correction C1 and C2           21         FSED         O         Detection monitor of the CD/DVD frame sync. signal           22         FAST         O         Playback speed monitor         N ch-OD output           24         DRF         O				
9   FSX				
10   SBCK	9	FSX	0	·
11				
12				•
13	$\overline{}$			
14     XIN     I     Connect a crystal resonator (16.9344MHz)       15     XOUT     O     Connect a crystal resonator       16     DVDD1     - 3.97 power supply of the oscillation circuit       17     EMPH     O     Monitor pin of the deemphasis       18     SBSY     O     Sync. signal output of the subcode block       19     DOUT     O     Audio EIAJ data output       20     EFLG     O     Error correction state monitor of the ED/DVD frame sync. signal       21     FSEQ     O     Detection monitor of the CD/DVD frame sync. signal       22     FAST     O     Playback speed monitor       23     V_PB     O     Monitor output of the rough servo/CLV control       24     DRF     O     In focus monitor       25     TEST3     1     Test input 3       26     TES     1     Tracking error signal input       27     TEST2     1     Test input 2       31TT     1     Jitter quantity detecting signal input       30     RF_PH     1     RF peak hold signal input       31     RF_BH     1     RF bottom hold signal input       32     TE     1     Fracking error signal input       33     FE     1     Focus error signal input    <	_			·
15 XOUT O Connect a crystal resonator 16 DVDD1 — 3.3V power supply of the oscillation circuit 17 EMPH O Monitor pin of the deemphasis 18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter ruganitity detecting signal input of EFM PLL 29 TILTE I Tilt error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 2 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSD - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking elondoutput 4 for SLC 55 SLCO2 O Spindle control signal output 56 SLCO O Spindle control signal output 57 SLCO O Spindle control signal output 58 SLCO O Spindle control signal output 59 SLCO O Spindle control signal output 50 SLCO O Spindle control signal output 51 SLCO O Spindle control signal output 52 SLCO O Spindle control signal output 53 SLCO O Spindle control signal output 54 SLCO O Spindle control signal output 55 SLCO O Spindle control signal output 56 SLCO O Spindle control signal output 57 SLCO O Spindle control signal output 58 SLCO O Force control signal output			1	·
16   DVDD1			0	
17 EMPH O Monitor pin of the deemphasis  8 SBSY O Sync. signal output of the subcode block  9 DOUT O Audio EIAJ data output  20 EFLG O Error correction state monitor of the error correction C1 and C2  11 FSEQ O Detection monitor of the CD/DVD frame sync. signal  12 FAST O Playback speed monitor N Nch-OD output  13 V_PB O Monitor output of the rough servo/CLV control  14 DRF O In focus monitor  15 TEST3 I Test input 3  16 TES I Tracking error signal input  17 TEST2 I Test input 2  18 JITT I Jitter quantity detecting signal input of EFM PLL  19 TILTE I Titt error signal input  10 RF_PH I RF peak hold signal input  11 FE I Tracking error signal input  12 TES I Tracking error signal input  13 RF_BH I RF bottom hold signal input  14 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC  15 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC  17 SLCO2 O Control output 1 for SLC  18 TEST1 I Test input 1  19 EFMERM + input  10 AVDD - SV power supply of A/D and D/A for servo  10 AVDD - SV power supply of A/D and D/A for servo  10 AVEF - Reference level of D/A for servo  10 VREF - Reference level of D/A for servo			_	•
18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Titt error signal input 30 RF_PH I RF beath hold signal input 31 RF_BH I RF bottom hold signal input 33 FE I Tracking error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 1 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFMEPH input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXCO O D Auxiliary output 44 TILDO O Tit control signal output 45 SLDO O Seled control signal output 46 SPDO O Focus control signal output 47 FDO O Fracking control signal output 48 TILDO O Tracking control signal output 49 VREF - Reference level of D/A for servo			0	
19   DOUT	18	SBSY	0	' '
20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 33 V_PB O Monitor output of the rough servo/CLV control 44 DRF O In focus monitor 55 TEST3 I Test input 3 56 TES I Tracking error signal input 57 TEST2 I Test input 2 58 JITT I Jitter quantity detecting signal input of EFM PLL 59 TILTE I Title rorr signal input 50 TR_PH I RF peak hold signal input 51 TR_BH I RF bottom hold signal input 52 TE I Tracking error signal input 53 TE I Tracking error signal input 53 FE I Focus error signal input 54 SLCIST1 — Current setting pin 1 of the constant current charge pump for SLC 55 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 56 SLCO1 O Control output 1 for SLC 57 SLCO2 O Control output 2 for SLC 58 TEST1 I Test input 1 50 SEMIN I EFM/EFM + input 51 AVSS — GND of A/D and D/A for servo 52 AUXO O DA auxiliary output 53 SLCO O Sied control signal output 54 SLDO O Sied control signal output 55 SLDO O Sied control signal output 56 SLDO O Tracking balance control signal output 57 SDO O Tracking control signal output 58 SLDO O Tracking control signal output 59 SPDO O Focus control signal output 50 SPDO O Tracking control signal output 51 SLDO O Tracking control signal output 52 SPDO O Tracking control signal output 53 SLDO O Tracking control signal output 54 SLDO O Tracking control signal output			0	
22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TITE I Titter or signal input 30 RR_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 RF_BH I Refocus error signal input 34 SLCIST1 — Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFMLEFM + input 40 AVDD — 5V power supply of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 42 AUXO O D A auxiliary output 44 TBAL O Tracking balance control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF — Reference level of D/A for servo			0	Error correction state monitor of the error correction C1 and C2
22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TITE I Titter or signal input 30 RR_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 RF_BH I Refocus error signal input 34 SLCIST1 — Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFMLEFM + input 40 AVDD — 5V power supply of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 42 AUXO O D A auxiliary output 44 TBAL O Tracking balance control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF — Reference level of D/A for servo	21	FSEQ	0	Detection monitor of the CD/DVD frame sync. signal
23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Tilt error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 1 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	22	FAST	0	
24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Tilt error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 1 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	23	V PB	0	
TEST I Tracking error signal input TeST2 I Test input 2  Reference level of D/A for servo  TILTE I Tracking error signal input of EFM PLL  TILTE I Tilt error signal input Tracking error signal input  Level of the constant current charge pump for SLC  Current setting pin 1 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC Current setting pin 2 of the constant current charge pump for SLC  Exploit the constant current charge pump for SLC  SLCOS O Control output 1 for SLC Test input 1  Exploit the constant current charge pump for SLC  AND O Control output 2 for SLC Tracking pin 2 of the constant current charge pump for SLC  AND O DA auxiliary output 1  AND O Tracking balance control signal output  Exploit the control signal output	24		0	· · · · · ·
Test   Tracking error signal input	25	TEST3	ı	Test input 3
27 TEST2	26	TES	ı	· ·
29 TILTE	27	TEST2	ı	
30   RF_PH	28	JITT	ı	Jitter quantity detecting signal input of EFM PLL
30   RF_PH	29	TILTE	I	Tilt error signal input
32 TE	30	RF_PH	ı	RF peak hold signal input
33 FE	31	RF_BH	ı	RF bottom hold signal input
34 SLCIST1 — Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD — 5V power supply of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF — Reference level of D/A for servo	32	TE	I	Tracking error signal input
35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD — 5V power supply of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF — Reference level of D/A for servo	33	FE	I	Focus error signal input
36 SLCO1 O Control output 1 for SLC  37 SLCO2 O Control output 2 for SLC  38 TEST1 I Test input 1  39 EFMIN I EFM/EFM + input  40 AVDD - 5V power supply of A/D and D/A for servo  41 AVSS - GND of A/D and D/A for servo  42 AUXO O DA auxiliary output  43 TILTDO O Tilt control signal output  44 TBAL O Tracking balance control signal output  45 SLDO O Spindle control signal output  46 SPDO O Spindle control signal output  47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	34	SLCIST1	_	Current setting pin 1 of the constant current charge pump for SLC
37 SLCO2 O Control output 2 for SLC  38 TEST1 I Test input 1  39 EFMIN I EFM/EFM + input  40 AVDD - 5V power supply of A/D and D/A for servo  41 AVSS - GND of A/D and D/A for servo  42 AUXO O DA auxiliary output  43 TILTDO O Tilt control signal output  44 TBAL O Tracking balance control signal output  45 SLDO O Sled control signal output  46 SPDO O Spindle control signal output  47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	35	SLCIST2	_	Current setting pin 2 of the constant current charge pump for SLC
38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	36	SLCO1	0	Control output 1 for SLC
39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	37	SLCO2	0	Control output 2 for SLC
40 AVDD - 5V power supply of A/D and D/A for servo  41 AVSS - GND of A/D and D/A for servo  42 AUXO O DA auxiliary output  43 TILTDO O Tilt control signal output  44 TBAL O Tracking balance control signal output  45 SLDO O Sled control signal output  46 SPDO O Spindle control signal output  47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	38	TEST1	I	Test input 1
41 AVSS - GND of A/D and D/A for servo  42 AUXO O DA auxiliary output  43 TILTDO O Tilt control signal output  44 TBAL O Tracking balance control signal output  45 SLDO O Sled control signal output  46 SPDO O Spindle control signal output  47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	39	EFMIN	I	EFM/EFM + input
42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	40	AVDD	-	5V power supply of A/D and D/A for servo
43 TILTDO O Tilt control signal output  44 TBAL O Tracking balance control signal output  45 SLDO O Sled control signal output  46 SPDO O Spindle control signal output  47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	41	AVSS	_	GND of A/D and D/A for servo
44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	42	AUXO	0	DA auxiliary output
45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	43	TILTDO	0	Tilt control signal output
46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	44	TBAL	0	Tracking balance control signal output
47 FDO O Focus control signal output  48 TDO O Tracking control signal output  49 VREF - Reference level of D/A for servo	45	SLDO	0	Sled control signal output
48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	46	SPDO	0	Spindle control signal output
49 VREF - Reference level of D/A for servo	47	FDO	0	
	48	TDO	0	Tracking control signal output
50 TEST4 I Test input 4	49	VREF	_	Reference level of D/A for servo
	50	TEST4	I	Test input 4

No.	Pin Name	I/O	Function
51	HFLIO	I/O	Mirror detection signal input/output
52	LASER	0	Output pin for laser ON/OFF control
53	PP0/DVD_CDB	I/O	General-purpose port input/output / Disc discrimination signal output
54	PP1/CRCERRB	I/O	General-purpose port input/output / Subcode CRC result signal output
55	FG	I	FG counter input
56	PP2/EVENT	I/O	General-purpose port input/output / Event counter input
57	RESB	ı	Reset input
58	CSB	ı	Chip select input
59	RDB	I	Internal state reading signal input
60	WRB	ı	Command / data writing signal input
61	DVDD2	-	5V power supply
62	VSS	_	GND
63	P0		
64	P1		
65	P2		
66	P3	1/0	Commenced / data institution to
67	P4	I/O	Command / data input/output
68	P5		
69	P6		
70	P7		
71	VSS	_	GND
72	DVDD1	_	3.3V power supply for internal
73	BUSYB	0	Busy signal output of command process
74	SQOUT	0	Serial output of subcode Q
75	CQCKB	ı	Shift clock input for subcode Q data output
76	RWC	I	Update permission input of subcode Q
77	WRQ	0	Read out ready monitor of subcode Q
78	AVSS	_	PLL GND for internal system clock
79	VRPFR	_	VCO oscillation range setting of PLL for system clock
80	VCOC	I	Compact a DLL filter for a rate of all all
81	VPDO	0	Connect a PLL filter for system clock
82	AVDD	_	PLL 5V power supply for system clock
83	PDO1	I/O	PLL filter connection pin 1 for EFM playback
84	PDO2	I/O	PLL filter connection pin 2 for EFM playback
85	PDO3	I/O	PLL filter connection pin 3 for EFM playback
86	AVSS		PLL GND for EFM playback
87	PCKIST1	_	Current setting 1 of PLL constant current charge pump for EFM playback
88	PCKIST2	_	Current setting 2 of PLL constant current charge pump for EFM playback
89	AVDD	_	PLL 5V power supply for EFM playback
90	DVDFR	-	VCO oscillation range setting of PLL for EFM playback 1
91	CDFR	_	VCO oscillation range setting of PLL for EFM playback 2
92	JV	0	Jitter output of PLL clock for EFM playback
93	PCK	0	Bit clock output for EFM playback
94	ADRAO	I	Address input
95	DVDSYEQ	ı	DVD synchronize pulse input
96	DVDSYNC	ı	DVD synchronous signal input
97	LEFM2	0	Output the state that cut and out a signal which was binary-stated value EFM with PCK 2
98	DVDD1	-	3.3V power supply for I/O
99	VSS	-	GND
100	LEFM	0	Output the state that cut and out a signal which was binary-stated value EFM with PCK 1
ь			

# ■ PD3410A (DVDM ASSY : IC11)

• System Control IC

# • Pin Function

No.	Mark	Pin Name	I/O	Function
1	XCS3/XCASL	XCS3	0	PD4995A (MY CHIP) chip select signal output
2	GND	GND	_	GND
3	СК	HCPUCK	0	N.C.
4	VCC	V+3D	_	V+3D
5	PICLK	_	I/O	N.C.
6	PIDATA	_	I/O	N.C.
7	GND	GND	-	GND
8	PORTH0	_	0	N.C.
9	PORTH1	_	0	N.C.
10	PORTH2	36MVH	0	BU2158F (Clock generator)
11	PORTH3	V_SEL2	0	Composite/S switching signal output of the skirt terminal [WY model]
12	VCC	V+3D	_	V+3D
13	PORTH4	_	0	N.C.
14	PORTH5	_	0	N.C.
15	PORTH6	_	0	N.C.
16	PORTH7	_	0	N.C.
17	GND	GND	-	GND
18	EXTAL	EXTAL	I	Connect a ceramic resonator
19	XTAL	XTAL	0	Connect a ceramic resonator
20	VCC	V+3D	_	V+3D
21	PORTG0	XCSDF0	0	DAC chip select signal output (←XLAT3)
22	PORTG1	XCSDF1	0	Chip select signal output for audio DSP microcomputer interface (←XLT_DACS)
23	PORTG2	_	0	N.C.
24	PORTG3	_	0	N.C.
25	PORTG4	LFEON	0	Select the mix to the front L ch and R ch of LFE element
26	GND	GND	_	GND
27	PORTG5	6CHMD	0	Switch the DAC outputs 2ch / 6ch (←XDVRST2)
28	PORTG6	XDASP	0	DASP through DASP output / to AV-1 output
29	PORTG7	XAMUTE	0	Last stage mute signal output of the audio
30	PORTF0	44X48	0	DAC 44/48 FS switching signal output
31	PORTF1	_	- 1	N.C.
32	PORTF2	3DON	0	3D audio ON/bypass switching signal output
33	VCC	V+3D	_	V+3D
34	PORTF3	_	0	N.C.
35	PORTF4	XAVSRST	0	Sync. reset port
36	PORTF5	DSCSNS	0	Disc detection pulse output L: Disc exist

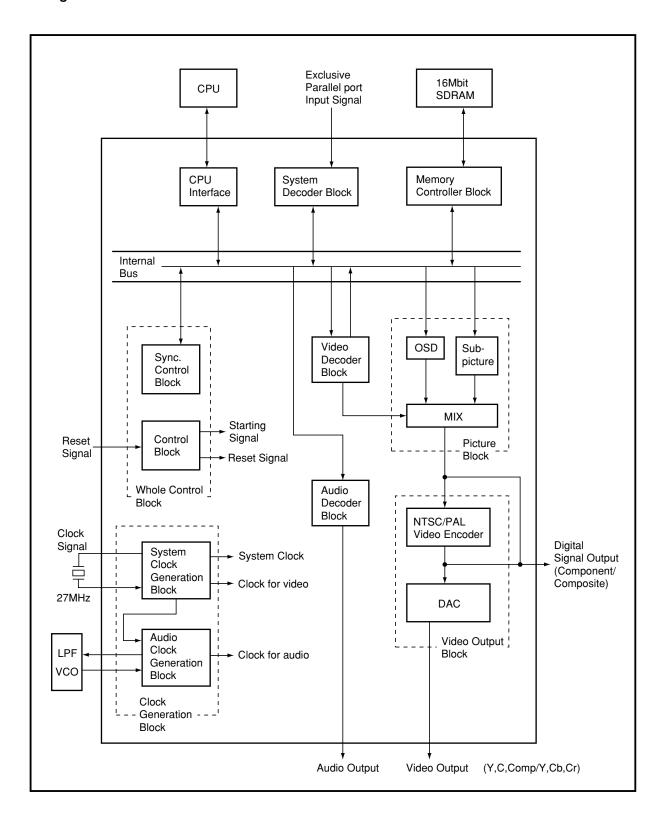
No.	Mark	Pin Name	I/O	Function
37	PORTF6	_	0	N.C.
38	PORTF7	XCSVE	0	Serial communication enable signal output of the video encoder [WY model]
39	GND	GND	_	GND
40	AVSS	GND	_	GND
41	AVCC	V+3D	_	V+3D
42	OUTA_P	LODRV	0	Loading drive output
43	VREF	V+3D	_	V+3D
44	OUTB_P	TEI	0	Tracking offset signal output
45	AVSS	GND	_	GND
46	AVSS	GND	_	GND
47	PORTE0	V_SEL	0	Component/composite switching signal output
48	PORTE1	_	I	N.C.
49	PORTE2	_	I	N.C.
50	PORTE3	FOFST1	I/O	Focus offset adjustment output 1
51	PORTE4	FOFST2	I/O	Focus offset adjustment output 2
52	PORTE5	XDFINH	I/O	Defect shunt signal output
53	PORTE6	DVD/XCD	0	DVD/CD switching signal output
54	PORTE7	LD1_ON	0	650 nm laser diode ON signal output
55	PORTD0	LD2_ON	0	780 nm laser diode ON signal output
56	VCC	V+3D	_	V+3D
57	PORTD1	DPD/TE	0	1 beam/3 beams switching signal output
58	PORTD2	AGOFF	0	AGC ON/OFF switching signal output of RF IC
59	PORTD3	XCD2X	0	Signal output for switching the double speed playback (VCD)
60	PORTD4	OEICG	0	OEIC gain switching signal output
61	GND	GND	_	GND
62	PORTD5	XMON	0	ON/OFF switching signal output of the spindle motor control output
63	PORTD6	_	0	N.C.
64	PORTD7	_	I	N.C.
65	PORTJ0	XDRVMUT	0	Driver mute output
66	PORTJ1	_	0	N.C.
67	PORTJ2	XDSPRST	0	Servo DSP reset
68	PORTJ3	SED0	I	Tray rotation drive
69	vcc	V+3D	_	V+3D
70	PORTJ4	TM_ENT	I	Test mode entry
71	PORTJ5	_	0	N.C.
72	PORTJ6	VSEL_SW	I	Component/composite SW input
73	PORTJ7	_	I	N.C.
74	PB0/TIOCA2	XCBUSY	I	Command busy input
75	PB1/TIOCB2	XABUSY	I	Auto-sequence busy input
76	PB2/TIOCA3	XINT2		Interrupt input 2 (AV-1)
77	VCC	V+3D	-	V+3D
78	PB3/TIOCB3	LT1	0	Communication response signal output to the FL controller
79	PB4/TIOCA4	SBSY		Subcode block sync. input
80	XMTEST	_		Test terminal (V+3D)
81	XCPUMD	- VDE057		Test terminal (V+3D)
82	XRES	XRESET		Reset input

83         GND         GND         -         GND           84         AN0         LODPOS         I         Loading position input           85         AN1         SLDPOS         I         Slider position input           86         AN2         CLAMP         I         Clamp position SW input           87         AN3         NAP_SW         I         NTSC/AUTO/PAL SW input           88         AN4         XOEM         I         Input terminal of OEM model protection           89         AN5         LDDEAD         I         Input for LD current value display           90         AN6         -         I         N.C.           91         AN7         -         I         N.C.           91         AN7         -         I         N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input	
85         AN1         SLDPOS         I Slider position input           86         AN2         CLAMP         I Clamp position SW input           87         AN3         NAP_SW         I NTSC/AUTO/PAL SW input           88         AN4         XOEM         I Input terminal of OEM model protection           89         AN5         LDDEAD         I Input for LD current value display           90         AN6         -         I N.C.           91         AN7         -         I N.C.           92         Avref         V+3D         -           93         AVCC         V+3D         -           94         AVS         GND         -           94         AVSS         GND         -           95         PB5/TIOCB4         TRYPOS         I Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I Communication request input from the FL controller           98         PB8/RxD0         SSI         I Serial data input (FL controller)           99         PB9/TxD0         SSO         O Serial data output (FL controller)	
86         AN2         CLAMP         I         Clamp position SW input           87         AN3         NAP_SW         I         NTSC/AUTO/PAL SW input           88         AN4         XOEM         I         Input terminal of OEM model protection           89         AN5         LDDEAD         I         Input for LD current value display           90         AN6         -         I         N.C.           91         AN7         -         I         N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RXD0         SSI         I         Serial data input (FL controller)           99         PB9/TXD0         SSO         O         Serial data output (FL controller)	
87         AN3         NAP_SW         I         NTSC/AUTO/PAL SW input           88         AN4         XOEM         I         Input terminal of OEM model protection           89         AN5         LDDEAD         I         Input for LD current value display           90         AN6         -         I         N.C.           91         AN7         -         I         N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
88         AN4         XOEM         I Input terminal of OEM model protection           89         AN5         LDDEAD         I Input for LD current value display           90         AN6         -         I N.C.           91         AN7         -         I N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I Communication request input from the FL controller           98         PB8/RxD0         SSI         I Serial data input (FL controller)           99         PB9/TxD0         SSO         O Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
89         AN5         LDDEAD         I Input for LD current value display           90         AN6         -         I N.C.           91         AN7         -         I N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I Communication request input from the FL controller           98         PB8/RxD0         SSI         I Serial data input (FL controller)           99         PB9/TxD0         SSO         O Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
90         AN6         -         I         N.C.           91         AN7         -         I         N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
91         AN7         -         I         N.C.           92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
92         Avref         V+3D         -         V+3D           93         AVCC         V+3D         -         V+3D           94         AVSS         GND         -         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
93         AVCC         V+3D         –         V+3D           94         AVSS         GND         –         GND           95         PB5/TIOCB4         TRYPOS         I         Disc number count input           96         PB6/TIOCXA4/TCLKC         C2F         I         C2 error input           97         PB7/TIOCXB4/TCLKD         XRDY         I         Communication request input from the FL controller           98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         –         V+3D	
94 AVSS GND - GND 95 PB5/TIOCB4 TRYPOS I Disc number count input 96 PB6/TIOCXA4/TCLKC C2F I C2 error input 97 PB7/TIOCXB4/TCLKD XRDY I Communication request input from the FL controller 98 PB8/RxD0 SSI I Serial data input (FL controller) 99 PB9/TxD0 SSO O Serial data output (FL controller) 100 VCC V+3D - V+3D	
95 PB5/TIOCB4 TRYPOS I Disc number count input 96 PB6/TIOCXA4/TCLKC C2F I C2 error input 97 PB7/TIOCXB4/TCLKD XRDY I Communication request input from the FL controller 98 PB8/RxD0 SSI I Serial data input (FL controller) 99 PB9/TxD0 SSO O Serial data output (FL controller) 100 VCC V+3D - V+3D	
96 PB6/TIOCXA4/TCLKC C2F I C2 error input 97 PB7/TIOCXB4/TCLKD XRDY I Communication request input from the FL controller 98 PB8/RxD0 SSI I Serial data input (FL controller) 99 PB9/TxD0 SSO O Serial data output (FL controller) 100 VCC V+3D - V+3D	
97 PB7/TIOCXB4/TCLKD XRDY I Communication request input from the FL controller  98 PB8/RxD0 SSI I Serial data input (FL controller)  99 PB9/TxD0 SSO O Serial data output (FL controller)  100 VCC V+3D - V+3D	
98         PB8/RxD0         SSI         I         Serial data input (FL controller)           99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
99         PB9/TxD0         SSO         O         Serial data output (FL controller)           100         VCC         V+3D         -         V+3D	
100 VCC V+3D - V+3D	
101 PB10/RxD1 RXD I Data input of the RS-232C	
102 PB11/TxD1 TXD O Data output of the RS-232C	
103 PB12/XIRQ4/SCK0 SSCK I/O Serial clock output (FL controller)	
104 PB13/XIRQ5/SCK1 XIRQL10 I Interrupt input #0 (MY CHIP)	
105 GND	
106 PB14/XIRQ6 XIRQL11 I Interrupt input #1 (MY CHIP)	
107 PB15/XIRQ7 XINT0 I Interrupt input #0 (AV-1)	
108 PA0/XCS4/TIOCA0 XCS4 O Servo DSP chip select signal output	
109 PA1/XCS5/XRAS – O N.C.	
110 PA2/XCS6/TIOCB0 XCS6 O AV-1 chip select signal output	
111 XWAIT XWAIT I Wait signal input	
112 XWRL XWRL O Write pulse output L	
113 GND	
114 XWRH XWRH O Write pulse output H	
115 XRD XRD O Read pulse output	
116 PA7/XBACK XCURDET I Over-current detection signal input	
117 PA8/XBREQ CTS I RS-232C transfer permit input	
118 PA9/XAH/XIRQOUT/ XADTRG DTR O RS-232C transfer permit output	
119 PA10/DPL/TIOCA1 XINT1 I Interrupt input 1 (AV-1)	
120 PA11/DPH/TIOCB1 THLD I Tracking hold signal input	
121 VCC V+3D – V+3D	
122 PA12/XIRQ0/DACK0/ TCLKA DACK0 O DMA response output (MY CHIP)	
123 PA13/XIRQ1/ XDREQ0/TCLKB XDREQ0 I DMA request input (MY CHIP)	
124 PA14/XIRQ2/XDACK1 XDACK1 O DMA response output (AV-1)	
125 PA15/XIRQ3/XDREQ1 XDREQ1 I DMA request input (AV-1)	
126 AD0 D0 I/O Data bus 0	

Section	No.	Mark	Pin Name	I/O	Function
129   102	127	GND	GND	_	GND
130	128	AD1	D1	I/O	Data bus 1
131   DP4	129	AD2	D2	I/O	Data bus 2
132	130	AD3	D3	I/O	Data bus 3
133   NDB	131	AD4	D4	I/O	Data bus 4
134	132	AD5	D5	I/O	Data bus 5
135   AD7	133	AD6	D6	I/O	Data bus 6
195	134	VCC	V+3D	-	V+3D
137   AD9	135	AD7	D7	I/O	Data bus 7
138   AD10	136	AD8	D8	I/O	Data bus 8
139   GND	137	AD9	D9	I/O	Data bus 9
140         AD11         D11         I/O         Data bus 11           141         AD12         D12         I/O         Data bus 12           142         AD13         D13         I/O         Data bus 13           143         AD14         D14         I/O         Data bus 14           144         VCC         V+3D         -         V+3D           145         AD15         D15         I/O         Data bus 15           146         A0 (XHBS)         AO         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         GND         GND           150         A3         A3         O         Address bus 3           151         A4         A4         A4         O         Address bus 4           152         A5         A5         A5         A5         A5         A6         Address bus 6           154         A7         A7         A7         O         Address bus 17         A1         A1         A1         A1         A1         A1	138	AD10	D10	I/O	Data bus 10
141         AD12         D12         I/O         Data bus 12           142         AD13         D13         I/O         Data bus 13           143         AD14         D14         I/O         Data bus 14           144         VCC         V-3D         - V+3D           145         AD15         D15         I/O         Data bus 15           146         AO (XHBS)         AO         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         - GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 3           152         A5         A5         O         Address bus 4           152         A5         A5         O         Address bus 6           154         A7         A7         A7         O         Address bus 17           155         A8         A8         O         Address bus 8           156         A9         A9         O         A	139	GND	GND	-	GND
142         AD13         D13         I/O         Data bus 13           143         AD14         D14         I/O         Data bus 14           144         VCC         V+3D         -         V+3D           145         AD15         D15         I/O         Data bus 15           146         A0 (XHBS)         A0         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         GND         GND           150         A3         A3         O         Address bus 3           151         A4         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 6           153         A6         A6         A6         O         Address bus 7           154         A7         A7         O         Address bus 8           155         A9         A9         O         Address bus 10           157         A10         A10         A10         Address bus 12           158         A1	140	AD11	D11	I/O	Data bus 11
143         AD14         D14         I/O         Data bus 14           144         VCC         V+3D         -         V+3D           145         AD15         D15         I/O         Data bus 15           146         A0 (XHBS)         A0         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         -         GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 3           151         A4         A4         O         Address bus 5           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 7           155         A8         A8         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 12           160         A13         A13         O <td>141</td> <td>AD12</td> <td>D12</td> <td>I/O</td> <td>Data bus 12</td>	141	AD12	D12	I/O	Data bus 12
144         VCC         V+3D         -         V+3D           145         AD15         D15         IO         Data bus 15           147         A1         A1         O         Address bus 0           148         A2         A2         O         Address bus 2           149         GND         GND         -         GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 7           155         A8         A6         O         Address bus 7           155         A8         A8         O         Address bus 9           157         A10         A10         O         Address bus 10           157         A10         A10         O         Address bus 11           159         A12         A12         O         Address bus 13           161         A14         A14         O         Address bus 13           161         A14         A14         O	142	AD13	D13	I/O	Data bus 13
145         AD15         D15         I/O         Data bus 15           146         AO (XHBS)         AO         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         -         GND           150         A3         A3         O         Address bus 3           151         A4         A4         A         Address bus 4           152         A5         A5         O         Address bus 6           153         A6         A6         O         Address bus 7           155         A8         A8         O         Address bus 17           155         A8         A8         O         Address bus 18           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 12           159         A12         A12         O         Address bus 13           160         A13         A13         O         Address bus 13           161         A14         A14	143	AD14	D14	I/O	Data bus 14
146         A0 (XHBS)         A0         O         Address bus 0           147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 7           155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 12           160         A13         A13         O         Address bus 12           161         A14         A14         O         Address bus 16           163         A16         A16         O <td< td=""><td>144</td><td>VCC</td><td>V+3D</td><td>-</td><td>V+3D</td></td<>	144	VCC	V+3D	-	V+3D
147         A1         A1         O         Address bus 1           148         A2         A2         O         Address bus 2           149         GND         GND         -         GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 7           155         A8         A8         A8         O         Address bus 9           156         A9         A9         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         A13         A13         A14           161         A14         A14         O         Address bus 15           162         A15         A15         O         Address bus 16           163         A16	145	AD15	D15	I/O	Data bus 15
148         A2         A2         O         Address bus 2           149         GND         GND         -         GND           150         A3         A3         O         Address bus 3           151         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 7           155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 13           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 15           162         A15         A15         O         Address bus 16           163         A16         A16	146	A0 (XHBS)	A0	0	Address bus 0
149         GND         GND         - GND           150         A3         A3         O Address bus 3           151         A4         A4         O Address bus 4           152         A5         A5         O Address bus 5           153         A6         A6         O Address bus 6           154         A7         A7         O Address bus 7           155         A8         A8         O Address bus 8           156         A9         A9         O Address bus 9           157         A10         A10         O Address bus 10           158         A11         A11         O Address bus 11           159         A12         A12         O Address bus 12           160         A13         A13         O Address bus 12           161         A14         A14         O Address bus 13           162         A15         A15         O Address bus 16           163         A16         A16         O Address bus 16           164         A17         A17         O Address bus 18           165         VCC         V+3D         V+3D           166         A18         A18         O Address bus 20 [RAM model] <td>147</td> <td>A1</td> <td>A1</td> <td>0</td> <td>Address bus 1</td>	147	A1	A1	0	Address bus 1
150         A3         A3         O         Address bus 3           151         A4         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 7           154         A7         A7         O         Address bus 8           155         A8         A8         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 18           165         VCC         V+3D         V+3D           167         A19         A19	148	A2	A2	0	Address bus 2
151         A4         A4         O         Address bus 4           152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 8           155         A8         A8         O         Address bus 9           156         A9         A9         O         Address bus 10           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         V+3D           166         A18         A18         O	149	GND	GND	-	GND
152         A5         A5         O         Address bus 5           153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 7           155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 10           157         A10         A10         O         Address bus 11           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 18           165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 19           168         A20         A20	150	A3	A3	0	Address bus 3
153         A6         A6         O         Address bus 6           154         A7         A7         O         Address bus 7           155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 13           162         A15         A15         O         Address bus 14           162         A15         A16         O         Address bus 15           163         A16         A16         O         Address bus 17           164         A17         A17         O         Address bus 17           165         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 19           168         A20 <t< td=""><td>151</td><td>A4</td><td>A4</td><td>0</td><td>Address bus 4</td></t<>	151	A4	A4	0	Address bus 4
154         A7         A7         O         Address bus 7           155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 20 [RAM model]           168         A20         A20         O         Address bus 20 [RAM model]           171         G	152	A5	A5	0	Address bus 5
155         A8         A8         O         Address bus 8           156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 13           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 20 [RAM model]           168         A20         A20         O         Address bus 20 [RAM model]           170         XNMI         XNMI         I         V+3D           171         GND <td>153</td> <td>A6</td> <td>A6</td> <td>0</td> <td>Address bus 6</td>	153	A6	A6	0	Address bus 6
156         A9         A9         O         Address bus 9           157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 20 [RAM model]           168         A20         A20         O         Address bus 20 [RAM model]           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10	154	A7	A7	0	Address bus 7
157         A10         A10         O         Address bus 10           158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 19           168         A20         A20         O         Address bus 20 [RAM model]           169         A21         A21         O         N.C.           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         O	155	A8	A8	0	Address bus 8
158         A11         A11         O         Address bus 11           159         A12         A12         O         Address bus 12           160         A13         A13         O         Address bus 13           161         A14         A14         O         Address bus 14           162         A15         A15         O         Address bus 15           163         A16         A16         O         Address bus 16           164         A17         A17         O         Address bus 17           165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 20 [RAM model]           168         A20         A20         O         Address bus 20 [RAM model]           169         A21         A21         O         N.C.           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         GND           173         XCS20         XCS20         O <td>156</td> <td>A9</td> <td>A9</td> <td>0</td> <td>Address bus 9</td>	156	A9	A9	0	Address bus 9
159       A12       A12       O Address bus 12         160       A13       A13       O Address bus 13         161       A14       A14       O Address bus 14         162       A15       A15       O Address bus 15         163       A16       A16       O Address bus 16         164       A17       A17       O Address bus 17         165       VCC       V+3D       - V+3D         166       A18       A18       O Address bus 18         167       A19       A19       O Address bus 19         168       A20       A20       O Address bus 20 [RAM model]         169       A21       A21       O N.C.         170       XNMI       XNMI       I V+3D         171       GND       GND       - GND         172       XCS10       -       O N.C.         173       XCS20       XCS20       O Chip select signal output of the GUI ROM [OEM model]         175       XCS23       O Chip select signal output of the SRAM	157	A10	A10	0	Address bus 10
160       A13       A13       O       Address bus 13         161       A14       A14       O       Address bus 14         162       A15       A15       O       Address bus 15         163       A16       A16       O       Address bus 16         164       A17       A17       O       Address bus 17         165       VCC       V+3D       -       V+3D         166       A18       A18       O       Address bus 18         167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the GUI ROM [OEM model]         175       XCS23       XCS23       O       Chip select signal output of the SRAM	158	A11	A11	0	Address bus 11
161       A14       A14       O       Address bus 14         162       A15       A15       O       Address bus 15         163       A16       A16       O       Address bus 16         164       A17       A17       O       Address bus 17         165       VCC       V+3D       -       V+3D         166       A18       A18       O       Address bus 18         167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the flash ROM         174       XCS23       XCS23       O       Chip select signal output of the SRAM	159	A12	A12	0	Address bus 12
162       A15       A16       O       Address bus 15         163       A16       A16       O       Address bus 16         164       A17       A17       O       Address bus 17         165       VCC       V+3D       -       V+3D         166       A18       A18       O       Address bus 18         167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the flash ROM         174       XCS22       -       O       Chip select signal output of the SRAM	160	A13	A13	0	Address bus 13
163       A16       A16       O       Address bus 16         164       A17       A17       O       Address bus 17         165       VCC       V+3D       -       V+3D         166       A18       A18       O       Address bus 18         167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the flash ROM         174       XCS22       -       O       Chip select signal output of the GUI ROM [OEM model]         175       XCS23       O       Chip select signal output of the SRAM	161	A14	A14	0	Address bus 14
164         A17         Address bus 17           165         VCC         V+3D         - V+3D           166         A18         A18         O Address bus 18           167         A19         A19         O Address bus 19           168         A20         A20         O Address bus 20 [RAM model]           169         A21         A21         O N.C.           170         XNMI         XNMI         I V+3D           171         GND         GND         - GND           172         XCS10         - O N.C.           173         XCS20         XCS20         O Chip select signal output of the flash ROM           174         XCS22         - O Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O Chip select signal output of the SRAM	162	A15	A15	0	Address bus 15
165         VCC         V+3D         -         V+3D           166         A18         A18         O         Address bus 18           167         A19         A19         O         Address bus 19           168         A20         A20         O         Address bus 20 [RAM model]           169         A21         A21         O         N.C.           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         O         N.C.           173         XCS20         XCS20         O         Chip select signal output of the flash ROM           174         XCS22         -         O         Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O         Chip select signal output of the SRAM	163	A16	A16	0	Address bus 16
166       A18       A18       O       Address bus 18         167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the flash ROM         174       XCS22       -       O       Chip select signal output of the GUI ROM [OEM model]         175       XCS23       O       Chip select signal output of the SRAM	164	A17	A17	0	Address bus 17
167       A19       A19       O       Address bus 19         168       A20       A20       O       Address bus 20 [RAM model]         169       A21       A21       O       N.C.         170       XNMI       XNMI       I       V+3D         171       GND       GND       -       GND         172       XCS10       -       O       N.C.         173       XCS20       XCS20       O       Chip select signal output of the flash ROM         174       XCS22       -       O       Chip select signal output of the GUI ROM [OEM model]         175       XCS23       O       Chip select signal output of the SRAM	165	VCC	V+3D	-	V+3D
168         A20         A20         O         Address bus 20 [RAM model]           169         A21         A21         O         N.C.           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         O         N.C.           173         XCS20         XCS20         O         Chip select signal output of the flash ROM           174         XCS22         -         O         Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O         Chip select signal output of the SRAM	166	A18	A18	0	Address bus 18
169         A21         A21         O         N.C.           170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         O         N.C.           173         XCS20         XCS20         O         Chip select signal output of the flash ROM           174         XCS22         -         O         Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O         Chip select signal output of the SRAM	167	A19	A19	0	Address bus 19
170         XNMI         XNMI         I         V+3D           171         GND         GND         -         GND           172         XCS10         -         O         N.C.           173         XCS20         XCS20         O         Chip select signal output of the flash ROM           174         XCS22         -         O         Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O         Chip select signal output of the SRAM	168	A20	A20	0	Address bus 20 [RAM model]
171         GND         GND         -         GND           172         XCS10         -         O         N.C.           173         XCS20         XCS20         O         Chip select signal output of the flash ROM           174         XCS22         -         O         Chip select signal output of the GUI ROM [OEM model]           175         XCS23         O         Chip select signal output of the SRAM	169	A21	A21	0	N.C.
172XCS10-ON.C.173XCS20XCS20OChip select signal output of the flash ROM174XCS22-OChip select signal output of the GUI ROM [OEM model]175XCS23XCS23OChip select signal output of the SRAM	170	XNMI	XNMI	I	V+3D
173XCS20XCS20OChip select signal output of the flash ROM174XCS22-OChip select signal output of the GUI ROM [OEM model]175XCS23XCS23OChip select signal output of the SRAM	171	GND	GND	-	GND
174     XCS22     -     O     Chip select signal output of the GUI ROM [OEM model]       175     XCS23     O     Chip select signal output of the SRAM	172	XCS10	_	0	N.C.
175 XCS23 XCS23 O Chip select signal output of the SRAM	173	XCS20	XCS20	0	Chip select signal output of the flash ROM
	174	XCS22	_	0	Chip select signal output of the GUI ROM [OEM model]
176 XCS2 – O N.C.	175	XCS23	XCS23	0	Chip select signal output of the SRAM
	176	XCS2	_	0	N.C.

# ■ MB86373B (DVDM ASSY : IC18)

- MPEG2 Decoder IC
- Block Diagram



# • Pin Function

No.	Pin Name	I/O	Function	No.	Pin Name	I/O	Function
1	CLKSEL	ı	ON/OFF signal of PLL ("H" : ON, "L" : OFF)	27	VDD	-	2.5V power supply
2	DIGCPN7	0	Digital component signal output (MSB) Digital Y signal output (9-bit) (MSB)	28	DIGCOMP4		
3	VSS	ı	GND	29	DIGCOMP3		Digital composite signal output Digital C signal output
4	DIGCPN6			30	DIGCOMP2	0	Digital C signal output
5	DIGCPN5			31	DIGCOMP1		
6	DIGCPN4	0	Digital component signal output Digital Y signal output (9-bit)	32	DIGCOMP0		Digital C signal output (LSB) Digital C signal output (LSB)
7	DIGCPN3		Digital Y Signal Output (9-bit)	33	DACK	0	27 MHz clock output
8	DIGCPN2			34	N.C.	-	Non connection
9	DIGCPN1			35	VSSA3	-	GND (D/A converter)
10	VDD	_	2.5V power supply	36	ANAC	0	Analog color (C) output signal
11	DIGCPN0	0	Digital component signal output (LSB) Digital Y signal output (9-bit) (LSB)	37	VDDA3	_	2.5V power supply (for built-in D/A converter only)
12	RBSEL	0	Cb and Cr discrimination signal at the digital component signal output. LSB at the digital Y signal output.	38	VSSA2	_	GND (D/A converter)
13	XHS	0	Horizontal sync. output signal	39	ANAY	0	Analog luminance (Y) output signal
14	xvs	0	Vertical sync. output signal	40	VDDA2	_	2.5V power supply (for built-in D/A converter only)
15	VSS	_	GND	41	VREF	ı	Reference voltage for D/A converter
16	XRESET	ı	LSI reset signal	42	VRO	0	Internal current setting pin of D/A converter
17	XLDCSYNC	I	External sync. signal input (LD mode)	43	VDDA4	_	2.5V power supply (for built-in D/A converter only)
18	KEY	0	KEY signal for LD and OSD overlay (LD mode)	44	VSSA1	_	GND (D/A converter)
19	PD	0	Phase comparison result output signal of horizontal sync. (LD mode)	45	ANACOMP	0	Analog composite output signal
20	VFLD	0	Field discrimination signal at the digital signal output H: even field L: odd field	46	VDDA1	_	2.5V power supply (for built-in D/A converter only)
21	DIGCOMP9		Digital composite signal output (MSB) Digital C signal output (MSB)	47	BF	0	Burst flag signal
22	DIGCOMP8			48	XBLK	0	H/V composite blanking signal
23	DIGCOMP7	0	Digital composite signal output	49	TEST4	0	Normally, set to "open".
24	DIGCOMP6		Digital C signal output	50	VSS	-	GND
25	DIGCOMP5			51	TEST0	I	Normally, set to "open".
26	VSS	_	GND	52	TEST1	I	"L" status normally

No.	Pin Name	I/O	Function	No.	Pin Name	I/O	Function		
53	DAIIN	ı	Digital data input of external input (SPDIF)	92	HADRS10	I	CPU address bus signal (MSB)		
54	CDDATA	I	Audio data input of external input (correspond to CD)	93	HADRS9				
55	CDLR	I	Data channel clock input of external input (correspond to CD)	94	HADRS8	ı	CPU address bus signal		
56	CDBCK	I	Data clock input of external input (correspond to CD)	95	HADRS7				
57	AODATA3			96	VSS	_	GND		
58	AODATA2	0	Audio decode data	97	VDD	_	2.5V power supply		
59	AODATA1			98	HADRS6				
60	VSS	_	GND	99	HADRS5		CPU address bus signal		
61	VDD	_	2.5V power supply	100	HADRS4	1	CPO address bus signal		
62	AODATA0	0	Audio decode data	101	HADRS3				
63	AOPCM	0	Digital audio interface output (compression data)	102	HADRS2		CPU address bus signal (LSB)		
64	AODAI	0	Digital audio interface output (decode data)	103	HDATA15		CPU data bus signal (MSB)		
65	LRCK	0	Data channel clock for D/A and digital filter	104	HDATA14	1/0			
66	AOMCK	0	Master clock for D/A and digital filter	105	HDATA13	1/0	CPU data bus signal		
67	BCK	0	Bit clock for D/A and digital filter	106	HDATA12				
68	TEST2		Normally, and to "an are"	107	VSS	-	GND		
69	TEST3	I	Normally, set to "open".	108	HDATA11				
70	NC	_	Non connection	109	HDATA10				
71	XDSPRST	1	Normally, set to "open".	110	HDATA9		OBU data hara alamat		
72	VSS	_	GND	111	HDATA8	1/0	CPU data bus signal		
73	TEST5	0	Normally, set to "open".	112	HDATA7				
74	NC			113	HDATA6				
75	NC			114	VDD	_	2.5V power supply		
76	NC	-	Normally, set to "open".	115	HDATA5				
77	NC	•		116	HDATA4				
78	SD7	ı	Parallel data input	117	HDATA3	1/0	CPU data bus signal		
79	VDD	_	2.5V power supply	118	HDATA2				
80	SD6			119	VSS	_	GND		
81	SD5	-		120	HDATA1		CPU data bus signal		
82	SD4		Parallel data input	121	HDATA0	1/0	CPU data bus signal (LSB)		
83	SD3	'	Taraner data input	122	BUSSEL	ı	Bus width selection signal (0 : 8-bit bus, 1 : 16-bit bus)		
84	SD2			123	XOSDACK	I	OSD data acknowledge signal		
85	VSS	_	GND	124	XOSDREQ	0	OSD data request signal		
86	SD1		B #44.4	125	HCPUSEL1	١.	CPU selection signal (00 :SPARC, 01 :86 system, 10 :68 system, 11 :Reserve		
87	SD0		Parallel data input	126	HCPUSEL0				
88	XERR	1	Error input signal	127	XINT3				
89	XSACK	ı	Acknowledge signal	128	XINT2	0	Interrupt request signal to the CPU		
90	XTEST	ı	Set to "H" at normal use	_	XINT1				
	SREQ	0	Data request signal		VSS	_	GND		

No.	Pin Name	I/O	Function	No.	Pin Name	I/O	Function	
131	VDD	_	2.5V power supply	170	XMDRCAS	0	CAS signal for SDRAM	
132	XINT0	0	Interrupt request signal to CPU	171	XMDRDQM1	0	Input mask / output enable signal for SDRAM	
133	XEXTRDY	0	SPARC, 68 system : Ready signal to CPU 86 system : Acknowledge (ACK) signal to CPU	172	VSS	ı	GND	
134	HRW	ı	CPU read / write signal	173	XMDRWE	0	Write enable signal for SDRAM	
135	HCLKIN	I	Host clock input	174	XMDRDQM0	0	Input mask / output enable signal for SDRAM	
136	XHCS	ı	LSI chip select signal	175	MDRDAT8	I/O	Data bus signal for SDRAM	
137	XHAS	_	SPARC, 68 system : CPU address strobe 86 system : CPU address status	176	VSS	ı	GND	
138	XHBE3			177	MDRDAT7			
139	XHBE2		CPU byte enable signal	178	MDRDAT9			
140	XHBE1	ļ	or o byte chable signal	179	MDRDAT6	I/O	Data bus signal for SDRAM	
141	XHBE0			180	MDRDAT10			
142	VSS	_	GND	181	MDRDAT5			
143	MDRADR4			182	VSS	_	GND	
144	MDRADR3	0	Address signal for SDRAM	183	VDD	-	2.5V power supply	
145	MDRADR5	O	Address signal for SDNAM	184	MDRDAT11			
146	MDRADR2			185	MDRDAT4			
147	VDD	_	2.5V power supply	186	MDRDAT12	I/O	Data bus signal for SDRAM	
148	VSS	_	GND	187	MDRDAT3			
149	MDRADR6			188	MDRDAT13			
150	MDRADR1		Address signal for SDRAM	189	VSS	_	GND	
151	MDRADR7	0		190	MDRDAT2			
152	MDRADR0		Address signal for SDRAM (LSB)	191	MDRDAT14	I/O	Data bus signal for SDRAM	
153	MDRADR8		Address signal for SDRAM	192	MDRDAT1	1/0		
154	VSS	-	GND	193	MDRDAT15		Data bus signal for SDRAM (MSB)	
155	TEST6			194	MDRDAT0	I/O	Data bus signal for SDRAM (LSB)	
156	TEST7	,	"L" status normally	195	VSS	_	GND	
157	TEST8	'	L status normany	196	N.C.	_	Non connection	
158	TEST9			197	ICK27M	- 1	System clock input	
159	MDRADR10		Address signal for SDRAM	198	VSS	-	GND	
160	MDRADR9	0	Address signal for obttAM	199	OCK27M	0	System clock output	
161	MDRADR11		Address signal for SDRAM (MSB)	200	VSSA(VCO)	ı	GND (for VCO only)	
162	XMDRCS	0	Chip select signal for SDRAM	201	VDDA(VCO)	-	2.5V power supply (for VCO only)	
163	MDRCKE	0	Clock enable signal for SDRAM	202	ILPF	0	PLL block inverter output for audio	
164	VSS	-	GND	203	MLPF	ı	PLL block inverter input for audio	
165	VDD	-	2.5V power supply	204	OLPF	0	Phase detector output for audio	
166	XMDRRAS	0	RAS signal for SDRAM	205	ovco	I	VCO input for audio clock	
167	MDRCLK	0	Clock output signal for SDRAM	206	VSS	-	GND	
168	VSS	_	GND	207	XPLLRST	I	PLL section reset signal	
169	MDRCLKIN	I	Clock input signal for SDRAM	208	XSYNCRST	- 1	SYNC reset signal	

# ■ PE5185A (FLKY ASSY : IC101)

# • FL Control IC

## • Pin Function

No.	Mark	Pin Name	I/O	Function	Active
1	P94	G7			
2	P93	G6			
3	P92	G5			
4	P91	G4	0	FL timing output	H: ON
5	P90	G3	1		
6	P81	G2			
7	P80	G1			
8	VDD	(5V)	_	-	
9	P27	FLSET1	١.		
10	P26	FLSET2	<b>┤</b>	FL tube setting	
11	P25	KEYSET	1	Key division number setting	
12	P24	(NC)	0	-	
13	P23	XREADY	0	Communication handshaking line with system control IC	L: Communication permission
14	P22	SCK	I/O	Communication clock output with system control IC	
15	P21	SO	I/O	Communication data output with system control IC	
16	P20	SI	ı	Communication data input with system control IC	
17	RESET	RESET IN	1	Reset input	L: Reset
18	P74	DISC5 LED		Disa Na LED ON/OFF	L. ON
19	P73	DISC4 LED	0	Disc No. LED ON/OFF	L: ON
20	AVSS	(GND)	-	-	
21	P17	(GND)	١,	(Ulpused)	
22	P16	(GND)	┤	(Unused)	
23	P15	_	ı	_	
24	P14	KIN2			
25	P13	KIN1	1	Key input	
26	P12	KIN0			
27	P11	MS1	ı	Inducing distinction input	
28	P10	MS0	1	Model distinction input	
29	AVDD	(5V)	_	-	
30	AVREF	(5V)	-	-	
31	P04	(GND)	I	(Unused)	
32	XT2	(NC)	-	-	
33	VSS	(GND)	-	-	
34	X1	X1	ı	Microcomputer clock connection	
35	X2	X2	-	Microcomputer clock connection	
36	P37				
37	P36	(NC)	0	_	
38	P35				
39	P34	(GND)	1	Microphone existence detection [RAM model]	H: Microphone having
40	P33	(NC)	0	-	

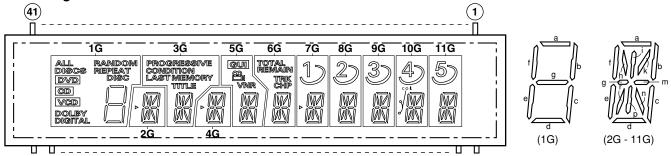
1	No.	Mark	Pin Name	I/O	Function	Active	
43   P30	41	P32	POWER ON	0	SW 5V ON/OFF	H: ON	
Harmonia   Harmonia	42	P31	RESET OUT	0	System reset output	L: Reset	
A5   P02	43	P30	(NC)	0	-		
Standing up	44	P03	TES	ı	Setting when system control IC is debugged	H: At debugging	
47   P00	45	P02	ON POWER	ı		L: STBY	
48   IC	46	P01	LT	T	Communication handshaking line with system control IC	H: Communication permission	
49   P72   DISC3 LED	47	P00	SEL IR	T	Remote control signal input		
SO   P71	48	IC	IC	-	-		
S1   P70   DISC1 LED	49	P72	DISC3 LED				
S2   VDD	50	P71	DISC2 LED	0	Disc No. LED ON/OFF	L: ON	
Signature   Sign	51	P70	DISC1 LED	1			
54         P126         OEM         O         OEM model distinction input         H: OEM           55         P125         LED (DVD)         O         FL OFF LED ON/OFF         H: ON           56         P124         LED (CD MODE)         O         FL OFF LED ON/OFF         H: ON           57         P123         LED (VNR)         O         TNR LED ON/OFF [DV-C603, DV-C36]         H: ON           58         P122         P18         P17         P16         P17         P16         P17         P18         P19	52	VDD	(5V)	-	-		
S5   P125	53	P127	P. ON LED	0	STANDBY LED ON/OFF	H: ON	
See   P124	54	P126	OEM	0	OEM model distinction input	H: OEM	
S7 P123	55	P125	LED (DVD)	0	FL OFF LED ON/OFF	H: ON	
SP   P123   LED (NR)   WRITED ON/OFF [DV-C603, DV-C36]	56	P124	LED (CD MODE)		CD mode LED ON/OFF		
Fig.	57	P123	LED (VNR)	10	VNR LED ON/OFF [DV-C603, DV-C36]	H: ON	
60         P120         P16           61         P117         P15           62         P116         P14           63         P115         P13           64         P114         P12           65         P113         P11           66         P112         P10           67         P111         P9           68         P110         P8           69         P107         P7           70         P106         P6           71         VLOAD         -27V         -           72         P105         P5           73         P104         P4           74         P103         P3           75         P102         P2           76         P101         P1           77         P100         G11           78         P97         G10           79         P96         G9     FL timing output  H: ON  H: ON  H: ON  H: ON  H: ON	58	P122	P18				
61         P117         P15           62         P116         P14           63         P115         P13           64         P114         P12           65         P113         P11           66         P112         P10           67         P111         P9           68         P100         P8           69         P107         P7           70         P106         P6           71         VLOAD         -27V         -           72         P105         P5           73         P104         P4           74         P103         P3           75         P102         P2           76         P101         P1           77         P100         G11           78         P97         G10           79         P96         G9       FL timing output   H: ON  H: ON  H: ON  H: ON  H: ON	59	P121	P17				
62       P116       P14         63       P115       P13         64       P114       P12         65       P113       P11         66       P112       P10         67       P111       P9         68       P100       P8         69       P107       P7         70       P106       P6         71       VLOAD       -27V       -         72       P105       P5         73       P104       P4         74       P103       P3       P3         75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9     FL timing output  H: ON  H: ON  H: ON	60	P120	P16	1			
63         P115         P13         P14         P12         P16         P113         P11         P17         P18         P110         P111         P10         P2         P10         P1         P100         G11         P100         G10         P10         P100         G10         P100         P100<	61	P117	P15				
64         P114         P12         P16         P113         P11         P16         P112         P10         P10         P111         P9         P101         P111         P9         P101         P100         G110         P100         G10         P100         G10         P100	62	P116	P14	1			
65         P113         P11           66         P112         P10           67         P111         P9           68         P110         P8           69         P107         P7           70         P106         P6           71         VLOAD         -27V         -           72         P105         P5           73         P104         P4         P4           74         P103         P3         P5           75         P102         P2         P2           76         P101         P1         P1           77         P100         G11         P1           78         P97         G10         G9           79         P96         G9         G9	63	P115	P13	1			
66         P112         P10           67         P111         P9           68         P110         P8           69         P107         P7           70         P106         P6           71         VLOAD         -27V         - Input for -27V         H: ON           72         P105         P5         F5         F5         F1           73         P104         P4         P4         F1         F2	64	P114	P12	0	FL segment output	H: ON	
67       P111       P9         68       P110       P8         69       P107       P7         70       P106       P6         71       VLOAD       -27V       -         72       P105       P5         73       P104       P4         74       P103       P3       P3         75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9     FL timing output  H: ON  H: ON	65	P113	P11	1			
68       P110       P8         69       P107       P7         70       P106       P6         71       VLOAD       -27V       - Input for -27V         72       P105       P5         73       P104       P4         74       P103       P3       P3         75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9     FL timing output  H: ON  H: ON	66	P112	P10	1			
69       P107       P7         70       P106       P6         71       VLOAD       -27V       - Input for -27V       H: ON         72       P105       P5       T3       P104       P4       P4       P4       P4       P4       P103       P3       P3       P102       P2       P2       P101       P1       P100       G11       P1       P100       G11       P100       G10       P100       G10       P100       P100 </td <td>67</td> <td>P111</td> <td>P9</td> <td>1</td> <td></td> <td></td>	67	P111	P9	1			
70         P106         P6           71         VLOAD         -27V         - Input for -27V         H: ON           72         P105         P5         - Input for -27V         H: ON           73         P104         P4         - P4         - P4         - P4         P103         P3         P3         P102         P2         P2         P2         P101         P1         P100         G11         P1         P100         G11         P100         G10         P100         G10         P100	68	P110	P8	1			
71         VLOAD         -27V         -         Input for -27V         H: ON           72         P105         P5         F5         F5<	69	P107	P7	1			
72       P105       P5         73       P104       P4         74       P103       P3         75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9    FL timing output  H: ON  H: ON	70	P106	P6	1			
73       P104       P4         74       P103       P3       P3         75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9    FL timing output H: ON	71	VLOAD	-27V	-	Input for -27V	H: ON	
74       P103       P3       O       FL segment output       H: ON         75       P102       P2       H: ON         76       P101       P1       P1         77       P100       G11       G10         78       P97       G10       G9         FL timing output       H: ON	72	P105	P5				
74       P103       P3       O       FL segment output       H: ON         75       P102       P2       H: ON         76       P101       P1       P1         77       P100       G11       G10         78       P97       G10       G9         FL timing output       H: ON	73	P104	P4	1			
75       P102       P2         76       P101       P1         77       P100       G11         78       P97       G10         79       P96       G9         FL timing output         H: ON			P3	0	FL segment output	H: ON	
77     P100     G11       78     P97     G10       79     P96     G9    FL timing output  H: ON	75		P2	1			
77     P100     G11       78     P97     G10       79     P96     G9    FL timing output  H: ON		P101		1			
78         P97         G10           79         P96         G9    FL timing output  H: ON	-						
79 P96 G9 H: ON				1_			
	-			10	FL timing output	H: ON	
				1			

# 7.2.2 FL DISPLAY

# ■ VAW1058 (FLKY ASSY: V101)

• FL DISPLAY

## Pin Assignment



## Anode Connection

	1G	2G	3G	4G	5G	6G	7G	8G	9G	10G	11G
P1	е	r	r	r	r	r	r	r	r	r	r
P2	d	n	n	n	n	n	n	n	n	n	n
Р3	С	g	g	g	g	g	g	g	g	g	g
P4	а	а	а	а	а	а	а	а	а	а	а
P5	g	b	b	b	р	b	b	b	р	b	b
P6	b	k	k	k	k	k	k	k	k	k	k
P7	f	h	h	h	h	h	h	h	h	h	h
P8	ALL	f	f	f	f	f	f	f	f	f	f
P9	\$	j	j	j	j	j	j	j	j	j	j
P10	DISC (DISC S)	m	m	m	m	m	m	m	m	m	m
P11	DVD	С	С	С	С	С	С	С	С	С	С
P12	CD	р	р	р	р	р	р	р	р	р	р
P13	VCD	е	е	е	е	е	е	е	Ф	е	е
P14	DOLBY DIGITAL	d	d	d	d	d	d	d	d	d	d
P15	DISC	$\supset$	TTLE	$\triangleright$	VINIR	CHP	$\triangleright$	_	_	col	_
P16	REPEAT	_	LAST MEMORY	_		TRK	<u></u>	<b>)</b>	9	$\supset$	
P17	RANDOM	_	CONDITION	_		REMAIN	1	2	M	4	5
P18	_	_	PROGRESSIVE	_		TOTAL	_	_	_	_	_

## Pin Connection

Pin No.	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21
Connection	F2	F2	NP	NP	P18	P17	P16	P15	P14	P13	P12	P11	P10	P9	P8	P7	P6	P5	P4	P3	P2
Pin No.	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
Connection	P1	NX	NX	11G	10G	9G	8G	NX	NX	7G	6G	5G	4G	3G	2G	1G	NP	NP	F1	F1	

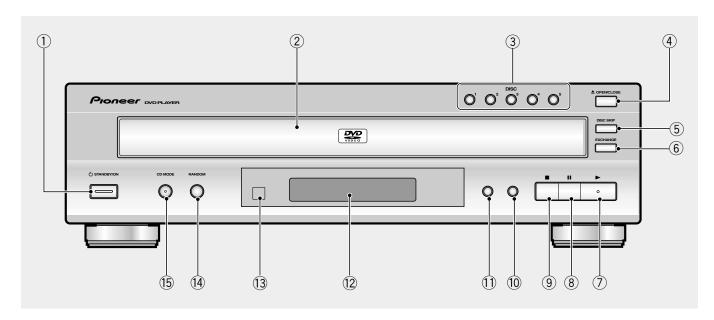
(4) DL Note (1) F1, F2 : Filament : Datum Line : No pin (5) 1G to 7G : Grid

(2) NP (3) NX : No extend pin

# 8. PANEL FACILITIES AND SPECIFICATIONS

# **8.1 PANEL FACILITIES**

## Front Panel



## 1 & STANDBY/ON button

Press to switch the player on or to put in standby.

## 2 Disc tray

When loading a disc, place it in the disc tray with the label side facing up.

### 3 DISC buttons

Use to select discs in the player directly.

#### (4) ▲ OPEN/CLOSE button

Press to open and close the disc tray.

## 5 DISC SKIP button

During playback, press to start playing the next disc in the player.

If the disc tray is open while no disc is playing, the tray rotates one disc space.

If the disc tray was opened using the **EXCHANGE** button, then pressing **DISC SKIP** rotates the disc tray two disc spaces.

#### **6** EXCHANGE button

Press during playback to open the disc tray without stopping playback. Press again to close the disc tray.

### ⑦ ► (play) button

Press to start or resume playback.

#### 8 II (pause) button

Press during playback to pause. Press again to resume playback.

## 

Press to stop playback. Pressing once enables playback to resume from a point shortly before the location where it stopped. Pressing twice causes the disc to return to the beginning of the disc if playback starts again.

## 10 ►► ►► (forward) button

Press to advance to chapters/tracks. Press and hold to perform fast-forward scanning.

#### 1) | << (reverse) button

Press to go back to previous chapters/tracks. Press and hold to perform reverse playback scanning.

## 12 Display window

Displays system information.

## 13 Remote sensor

Point the remote control toward the remote sensor to operate the player.

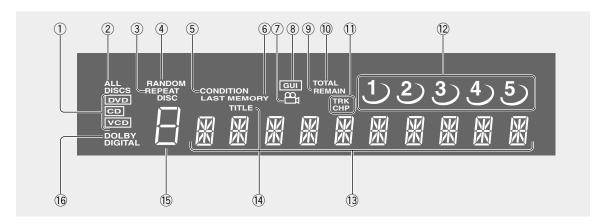
## (14) RANDOM button

Press to play discs, titles, chapters or tracks in random order.

#### (15) CD MODE button and indicator

Switches CD mode on/off. The indicator lights when on.

# **Display Window**



#### 1 DVD indicator

Lights when a DVD is loaded.

#### **CD** indicator

Lights when an audio CD is loaded.

#### **VCD** indicator

Lights when a Video CD is loaded.

## 2 1 DISC/ALL DISCS indicator

Lights during random or repeat play. If ALL Discs is selected, **ALL DISCS** lights; if 1 Disc is selected, **DISC** lights.

#### 3 REPEAT indicator

Indicates that the Repeat function is on.

#### 4 RANDOM indicator

Indicates that the Random function is on.

# (5) CONDITION indicator

Indicates that Condition Memory settings are memorized for the currently loaded DVD.

## **6** LAST MEMORY indicator

Indicates the Last Memory location is recorded in memory for the currently loaded DVD or Video CD.

## (angle) Indicator

Indicates Multi-Angle playback is in progress.

#### 8 GUI indicator

Indicates an on-screen menu operation is being performed.

## 9 REMAIN indicator

Indicates that the remaining playback time of a title or chapter/track is being displayed.

#### 10 TOTAL indicator

Indicates that the total number of titles/chapters or tracks is being displayed.

#### 11) TRK indicator

Indicates a track number is being displayed.

#### **CHP** indicator

Indicates a chapter number is being displayed.

### 12 Disc tray/Disc indicator

→ means that a disc is loaded in that position. In CD mode, → indicates a CD. (However, if the player has not yet determined whether a disc is loaded it also lights.)

## 13 Counter display

Displays the playback mode, type of disc, title and chapter/track numbers, playback time, etc.

#### (14) TITLE indicator

Indicates a title number is being displayed.

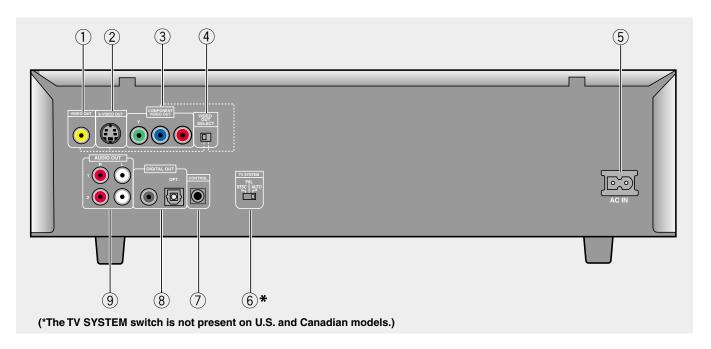
## 15 DISC number indicator

Shows the disc number currently in the play position.

#### 16 DOLBY DIGITAL indicator

Indicates Dolby Digital audio playback.

## Rear Panel



### (1) VIDEO OUT jack

Connect to the video input on a TV or monitor or AV amplifier or receiver with video input capability.

When using this output, be sure to set **VIDEO OUT SELECT** to the left position.

#### 2 S-VIDEO OUT jack

If your TV or monitor has an S-video input, clear picture reproduction is possible by connecting the player to your TV or monitor via the S-Video jack.

When using this output, be sure to set **VIDEO OUT SELECT** to the left position.

# **③ COMPONENT VIDEO OUT jacks**

If your TV or monitor has component video inputs, you can produce a higher quality picture on your TV or monitor by connecting to the component video outputs on this unit. When using these jacks, be sure to set **VIDEO OUT SELECT** to the right position.

#### (4) VIDEO OUT SELECT switch

Use to set which output is used to output the video signals. Select either video/S-video or component video signal output depending on the connections you make.

(5) AC IN power cord connection terminal (plug varies with the country/region, so may not match the illustration)
Use to connect the power cord to the wall outlet.

# (6) TV SYSTEM switch\* (except for U.S. and Canadian models)

Use to change the TV signal mode to either PAL or NTSC according to the type of TV and disc to be used. When the switch is in the **AUTO** position, the player outputs the format on the disc as is.

#### 7 CONTROL IN jack

Use to connect this player to another component bearing the Pioneer mark. This lets you control this unit as though it were a component in a system. Player operations are then performed by pointing the remote control at the component that the player is connect to.

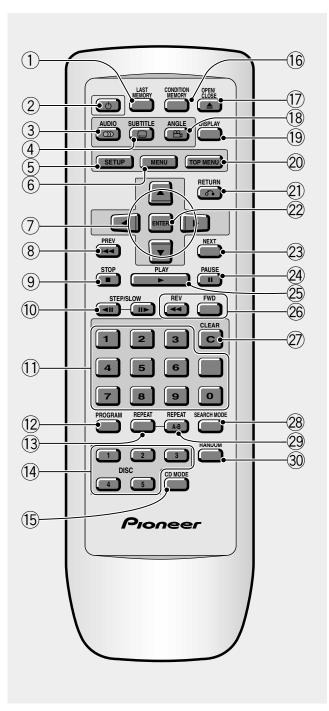
#### 8 DIGITAL OUT jacks (coaxial, optical (OPT.))

Use to output the digital audio signal recorded on discs. You can output the digital signal via either coaxial or optical output jack to an AV amplifier or receiver.

### 9 AUDIO OUT jacks

Use to output two-channel audio (analog) to the audio stereo inputs on a TV or stereo amplifier. If you are connecting to a receiver that has both digital and analog input jacks for DVD player connection, it may be beneficial to make both connections.

## Remote Control Unit



(Buttons indicated with \* are used for menu operation.)

## 1) LAST MEMORY button

You can resume DVD or Video CD playback from the point you last watched even if the disc is removed from the player. Press **LAST MEMORY** during playback to set a Last Memory point. When you want to resume playback of that disc, press **LAST MEMORY** in the stop mode and playback starts from the memorized point. Last Memory locations can be stored for up to 5 DVDs and 1 Video CD.

## 2 (standby/on) button

Press to switch the player on or to put in standby.

## 3 AUDIO O) button

Press repeatedly to select one of the audio languages programmed on a DVD.

For Video CD and CD, each press changes the audio output as follows.

# 4 SUBTITLE .... button

Press repeatedly to select one of the subtitle languages programmed on a DVD or to turn the subtitles off.

#### (5) SETUP button\*

Press when the player is in either play or stop mode to open and close the Setup screen.

## 6 MENU button\*

Use to display or close the DVD menu screen.

#### ⑦ Cursor buttons (◄/►/▲/▼)\*

Use to move through the options on menu screens and to change settings.

## 8 PREV ⊢ button (previous)

During playback, press **PREV** I◀◀ to go back to a previous chapter/track.

## 9 STOP ■ button

Press to stop playback. Pressing once enables playback to resume from a point shortly before the location where it was stopped. Pressing twice causes the disc to return to the beginning of the disc when playback starts again.

### 10 STEP/SLOW **◄II/II►** buttons

Press **STEP/SLOW II►** during playback to view slow playback. In pause mode, press **STEP/SLOW II►** to advance DVDs and Video CDs frame by frame and **STEP/SLOW II** to back up a few frames at a time.

### 11 Number buttons (1-9, 0, +10)\*

Use to perform direct title and chapter/track searches, and to input numerical values.

#### 12 PROGRAM button

You can program titles, chapters, or tracks to play back in a desired order. Programs can be a maximum of 24 steps.

### 13 REPEAT button

During playback of a DVD, press once to repeat playback of the current chapter, and twice to repeat playback of the current title. During playback of a Video CD or CD, press once to repeat playback of the current track, twice to repeat playback of the current disc, and 3 times to repeat all the discs loaded in the player.

#### (14) DISC buttons (1-5)

Use to select discs in the player directly.

#### (15) CD MODE button

Switches CD mode on/off.

## **16 CONDITION MEMORY button**

You can store in memory the settings for up to 15 DVDs. Press **CONDITION MEMORY** during DVD playback to memorize the settings.

## **17** OPEN/CLOSE **≜** button

Press to open or close the disc tray.

# 18 ANGLE 20 button

#### 19 DISPLAY button

Press during playback to display statistical disc information. Press repeatedly to display different information.

### 20 TOP MENU button\*

Press to call up the top menu programmed on the DVD. Depending on the DVD, the top menu may be identical to the DVD menu.

#### 21 RETURN 5 button\*

Use to go one menu back (current settings are maintained). Use **RETURN** & when you do not want to change the option setting in a menu.

## 22 ENTER button\*

Use to implement settings selected with the cursor buttons or to set items highlighted in a menu.

## 23 NEXT ►►I button

During playback, press **NEXT** ►►I to advance to the next chapter/track.

#### 24 PAUSE II button

Press to pause playback of a disc. Press again to resume playback.

#### 25 PLAY ▶ button

Press to start disc playback.

### 26 ◄ REV/FWD ▶► (fast reverse/forward) buttons

During playback of DVD and Video CD, press **FWD** ►► to perform fast forward scanning. Press **REV** ◀◀ to perform fast reverse scanning of DVD and Video CD. When a CD is loaded, audio scanning is performed.

#### ② CLEAR button (C)

Works in conjunction with a number of player functions. Use to cancel repeat and random playback, and to edit programs.

#### 28 SEARCH MODE button

Press to perform a title, chapter/track or elapsed time search.

### 29 REPEAT A-B button

Press at the beginning and end of the section you want to repeat or to mark a location you want to return to.

#### 30 RANDOM button

Press to play discs, titles, chapters or tracks in random order.

# **8.2 SPECIFICATIONS**

General	
System DVD	system, Compact Disc digital video system
	and Compact Disc digital audio system
Power requirements	
	s AC 120 V, 60 Hz
	AC 110-127/220-240 V, 50/60 Hz
Power consumption	s 11 W
	by modeless than 1 W
	420 (W) x 375 (D) x 128 (H) mm
	$(16^{9}/_{16} (W) \times 14^{13}/_{16} (D) \times 5^{1}/_{16} (H) in.)$
	(Not including protruding cables, etc.)
	+5°C to +35°C (+41°F to +96°F)
Operating humidity	5% to 85% (no condensation)
S-Video output	
	1 Vp-p (75 Ω)
Jack	S-VIDEO jack
Video output	
•	1 Vp-p (75 Ω)
Jack	RCA jack
Component video output	
$(Y, P_B, P_R)$	
Output level	Υ: 1.0 Vp-p (75 Ω)
	P <sub>B</sub> , P <sub>R</sub> : 0.7 Vp-p (75 Ω)
lacks	PCA jacks
	RCA jacks
Audio output (2 pairs)	•
Audio output (2 pairs) Output level	200 mVrms (1 kHz, –20 dB)
Audio output (2 pairs) Output level Number of channels	200 mVrms (1 kHz, –20 dB)
Audio output (2 pairs) Output level Number of channels Jacks	
Audio output (2 pairs) Output level Number of channels Jacks Digital audio characteristic	
Audio output (2 pairs) Output level Number of channels Jacks Digital audio characteristic Frequency response	
Audio output (2 pairs) Output level Number of channels Jacks  Digital audio characteristic Frequency response S/N ratio	
Audio output (2 pairs) Output level Number of channels Jacks Digital audio characteristic Frequency response S/N ratio Dynamic range	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level Number of channels Jacks  Digital audio characteristic Frequency response S/N ratio Dynamic range Total harmonic distortion Wow and flutter	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	
Audio output (2 pairs) Output level	

#### Note

The specifications and design of this product are subject to change without notice, due to improvement.

- \* Manufactured under license from Dolby Laboratories. "Dolby" and the double-D symbol are trademarks of Dolby Laboratories. Confidential unpublished works. © 1992-1997 Dolby Laboratories. All rights reserved.
- \* "DTS" is trademark of Digital Theater Systems, Inc.

## **Accessories**

Please confirm that the following were received with the player. Audio cord (VDE1054)



Video cord (VDE1055)



#### Power cord

(plug varies with the country/region, so may not match the illustration)

(KUXQ, KCXQ Types : ADG7022)

(RDXQ1/RA, RDXQ/RD Types: ADG1158)



Remote control unit (VXX2705)



AA (R6P) dry cell batteries



**Operating instructions**